



# PJM65H02NTE

## N-Channel Enhancement Mode Power MOSFET

### Product Summary

- $V_{DS} = 650V, I_D = 2A$
- $R_{DS(on)} < 5.3\Omega @ V_{GS} = 10V$

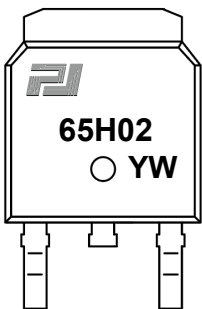
### Features

- Advanced Planar Technology
- 100% Avalanche Tested
- RoHS and Reach Compliant
- Halogen and Antimony Free
- Moisture Sensitivity Level 3

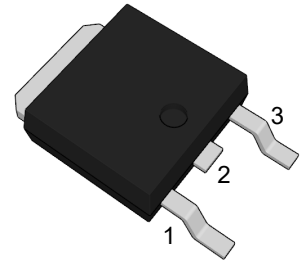
### Application

- Load Switch
- PWM Application
- Power management

### Marking Code



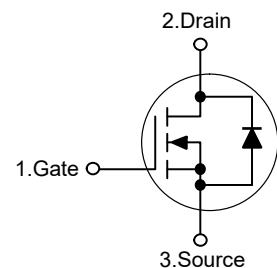
### TO-252



(Top View)

Pin	Description
1	Gate
2	Drain
3	Source

### Schematic Diagram



### Absolute Maximum Ratings

Ratings at 25°C case temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	650	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Drain Current-Continuous	$I_D$	2	A
Drain Current-Pulsed <sup>Note1</sup>	$I_{DM}$	8	A
Single Pulse Avalanche Energy <sup>Note2</sup>	$E_{AS}$	3.2	mJ
Maximum Power Dissipation	$P_D$	35	W
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-55 to +150	°C

### Thermal Characteristics

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	3.6	°C/W
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### Electrical Characteristics

(T<sub>J</sub>=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	650	--	--	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V	--	--	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	--	--	±100	nA
Gate Threshold Voltage <sup>Note3</sup>	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	3.5	4.5	5.5	V
Drain-Source On-Resistance <sup>Note3</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =1A	--	4.7	5.3	Ω
Forward Transconductance <sup>Note3</sup>	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =1A	1	--	--	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz	--	381	--	pF
Output Capacitance	C <sub>oss</sub>		--	35.5	--	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		--	6	--	pF
Gate Resistance	R <sub>G</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	--	4.5	--	Ω
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =520V, I <sub>D</sub> =2A, V <sub>GS</sub> =10V	--	9.5	--	nC
Gate-Source Charge	Q <sub>gs</sub>		--	1.5	--	nC
Gate-Drain Charge	Q <sub>gd</sub>		--	4.9	--	nC
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =325V, I <sub>D</sub> =2A, R <sub>GEN</sub> =10Ω	--	11	--	nS
Turn-on Rise Time	t <sub>r</sub>		--	13	--	nS
Turn-off Delay Time	t <sub>d(off)</sub>		--	29	--	nS
Turn-off Fall Time	t <sub>f</sub>		--	12	--	nS
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>Note3</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =2A	--	--	1.4	V
Diode Forward Current	I <sub>S</sub>		--	--	2	A

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. EAS Condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=100V, V<sub>G</sub>=10V, L=10mH, R<sub>G</sub>=25Ω, I<sub>AS</sub>=0.8A.

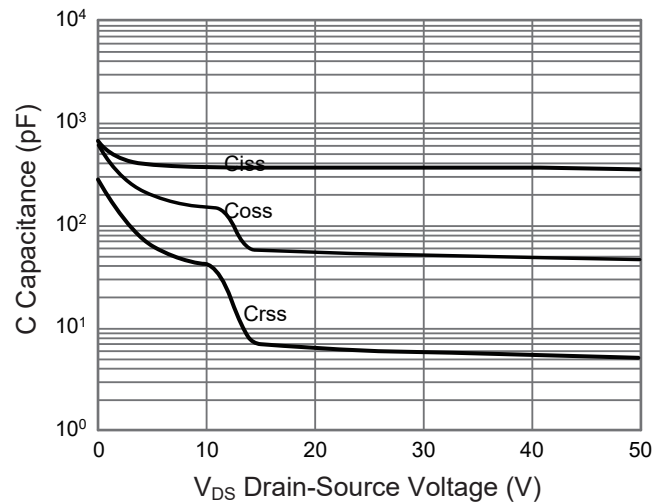
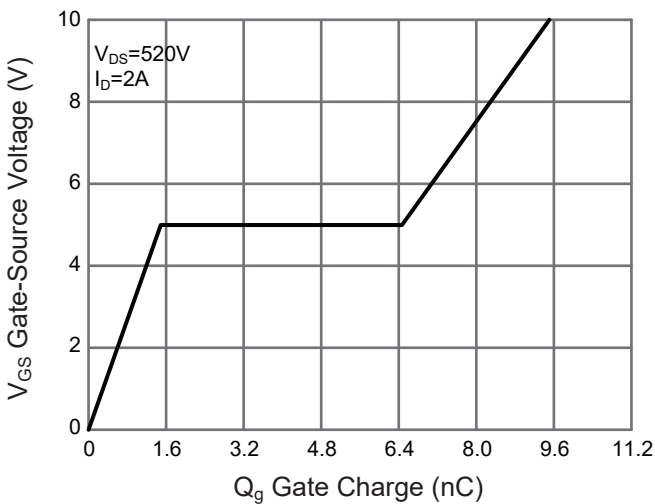
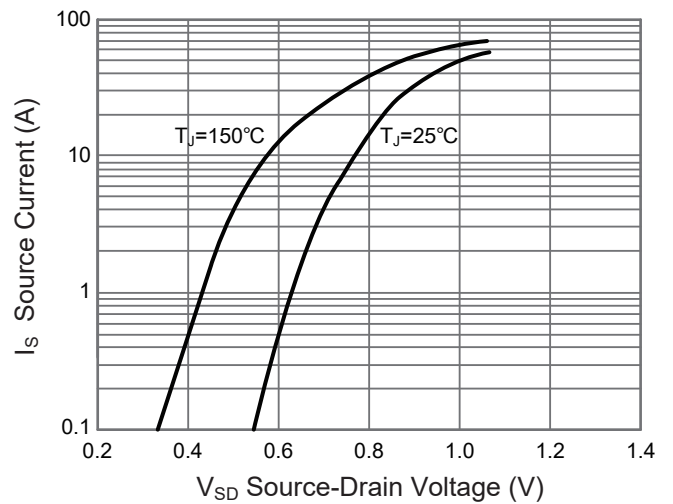
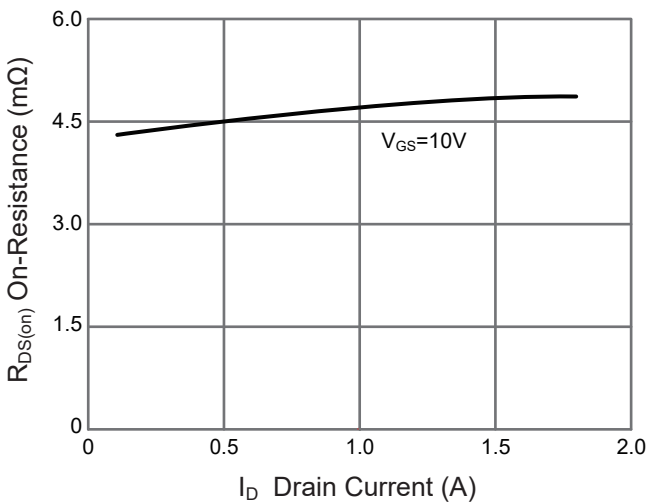
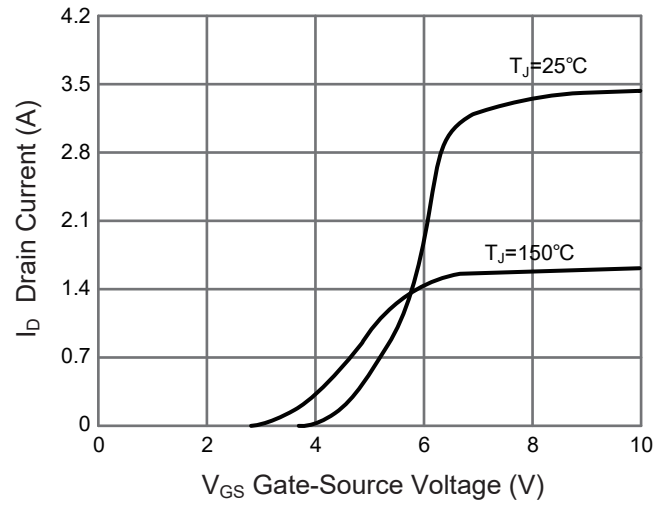
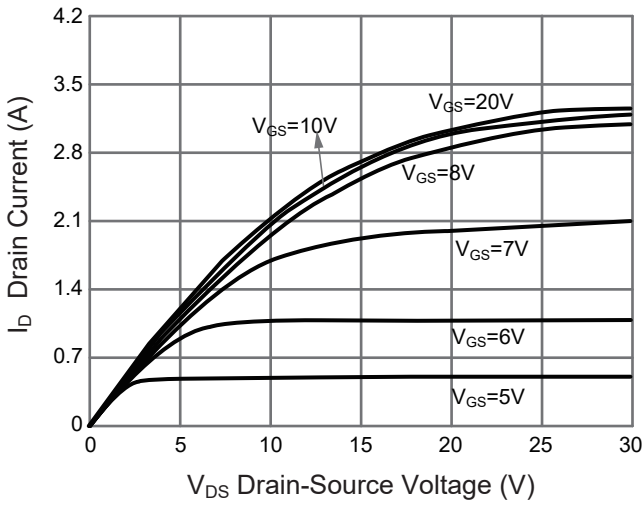
3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.



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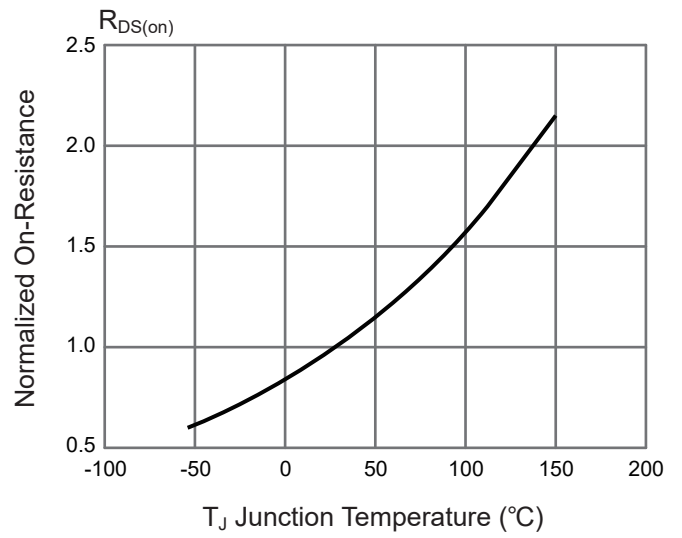
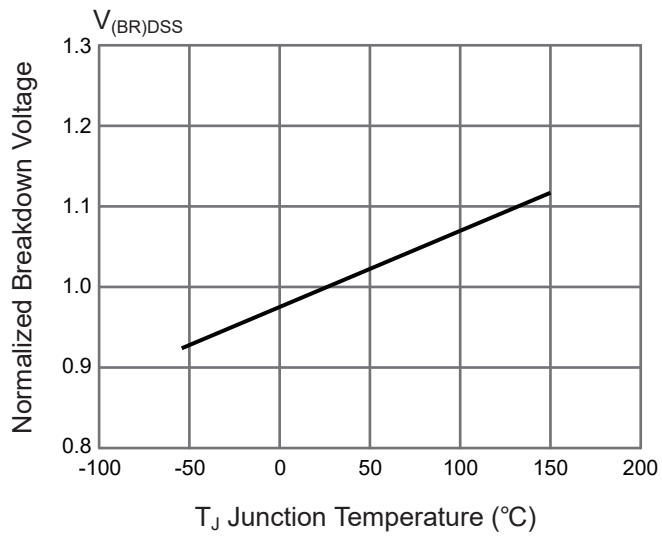
### Typical Characteristic Curves





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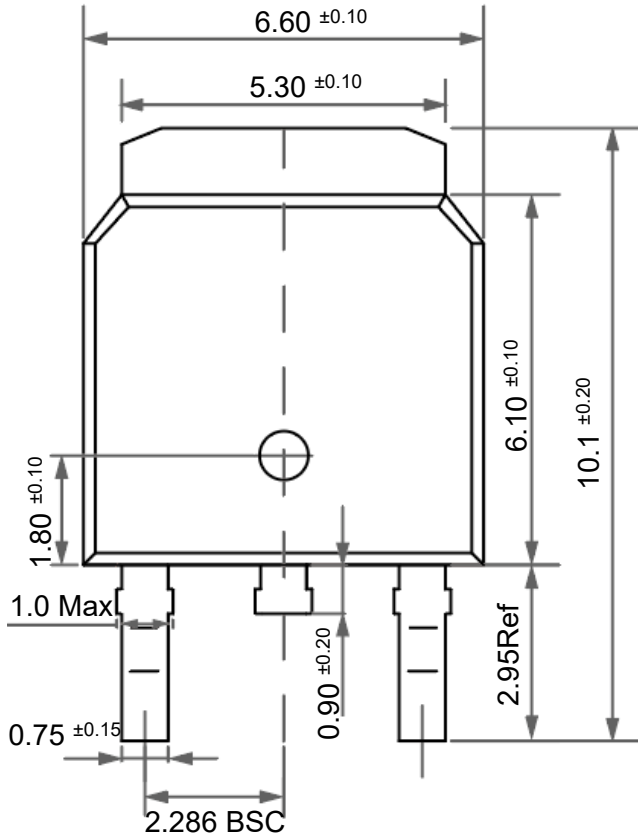
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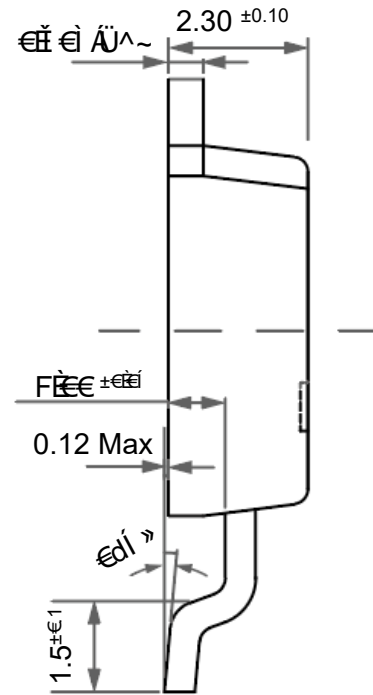
### Package Outline

TO-252

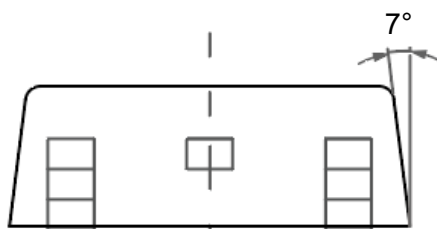
Dimensions in mm



Front View



Side View



Bottom View

### Ordering Information

Device	Package	Shipping
PJM65H02NTE	TO-252	2,500PCS/Reel&13inches