



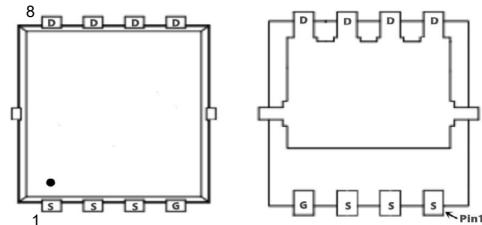
PJM20P30DL

P-Channel Enhancement Mode Power MOSFET

Features

- Excellent $R_{DS(ON)}$ and Low Gate Charge
- $V_{DS} = -30V, I_D = -20A$
- $R_{DS(on)} < 25m\Omega @ V_{GS} = -10V$

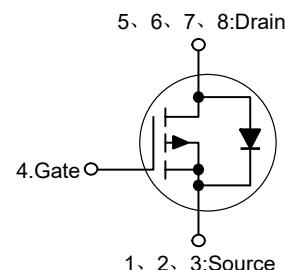
PDFN3x3-8L



Applications

- Lithium battery protection
- Wireless impact
- Mobile phone fast charging

Schematic Diagram



Absolute Maximum Ratings

Ratings at 25°C Case temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$-V_{DS}$	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous and $V_{GS}=10V$ Note1	$-I_D$	20	A
Drain Current-Pulsed Note2	$-I_{DM}$	80	A
Single Pulse Avalanche Energy Note3	E_{AS}	16	mJ
Avalanche Current	I_{AS}	17	A
Maximum Power Dissipation Note4	P_D	16.6	W
Junction Temperature	T_J	150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

Thermal Characteristics

Thermal Resistance, Junction-to-Ambient Note1	$R_{\theta JA}$	7.53	°C/W
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Electrical Characteristics

($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$-V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=-250\mu\text{A}$	30	32	--	V
Zero Gate Voltage Drain Current	$-I_{\text{DSS}}$	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
Gate Threshold Voltage ^{Note2}	$-V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=-250\mu\text{A}$	1.2	1.7	2.5	V
Drain-Source On-Resistance ^{Note2}	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=-10\text{V}, I_D=-10\text{A}$	--	18.8	25	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_D=-5\text{A}$	--	30.5	40	$\text{m}\Omega$
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	900	--	pF
Output Capacitance	C_{oss}		--	140	--	pF
Reverse Transfer Capacitance	C_{rss}		--	120	--	pF
Switching Characteristics						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-15\text{V}, I_D=-15\text{A}, V_{\text{GS}}=-10\text{V}, R_G=3.3\Omega$	--	6	--	nS
Turn-on Rise Time	t_r		--	5	--	nS
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	25	--	nS
Turn-off Fall Time	t_f		--	7	--	nS
Total Gate Charge	Q_g	$V_{\text{DS}}=-15\text{V}, I_D=-15\text{A}, V_{\text{GS}}=-4.5\text{V}$	--	19	--	nC
Gate-Source Charge	Q_{gs}		--	6.3	--	nC
Gate-Drain Charge	Q_{gd}		--	4.5	--	nC
Source-Drain Diode Characteristics						
Diode Forward Voltage ^{Note2}	$-V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_s=-1\text{A}$	--	--	1.2	V
Diode Forward Current ^{Note1,5}	$-I_s$		--	--	20	A

Note :

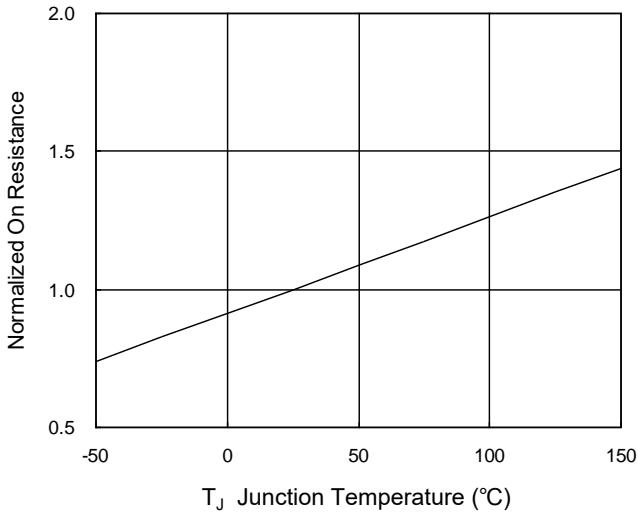
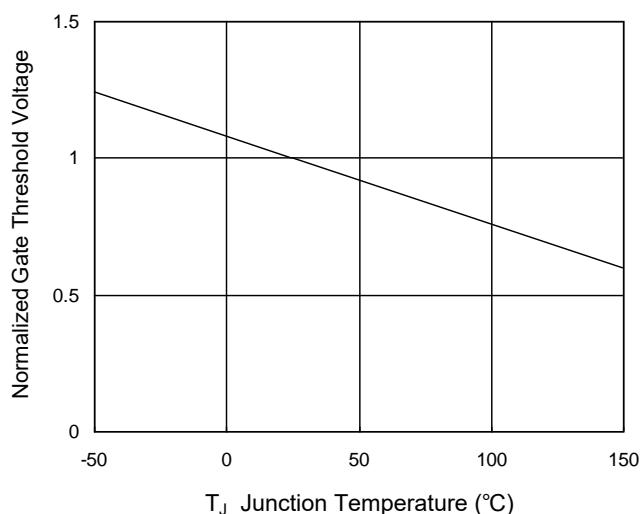
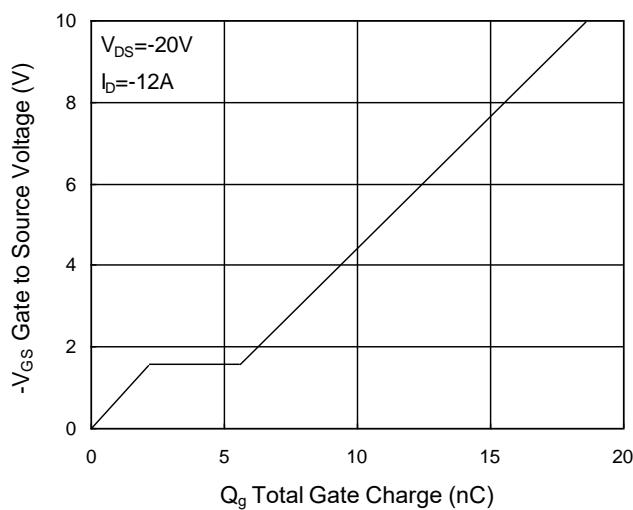
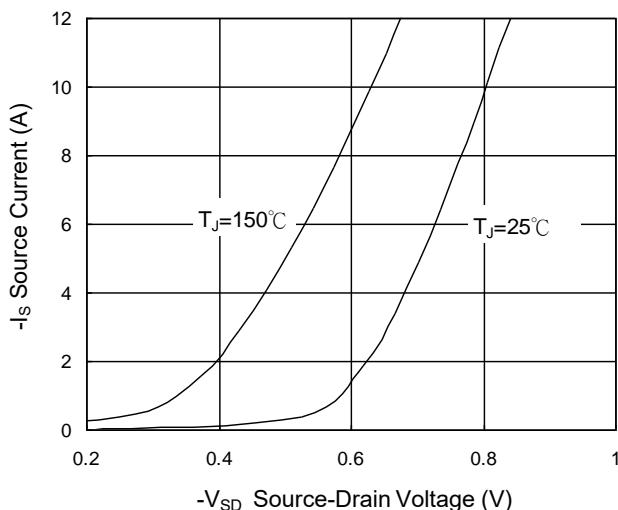
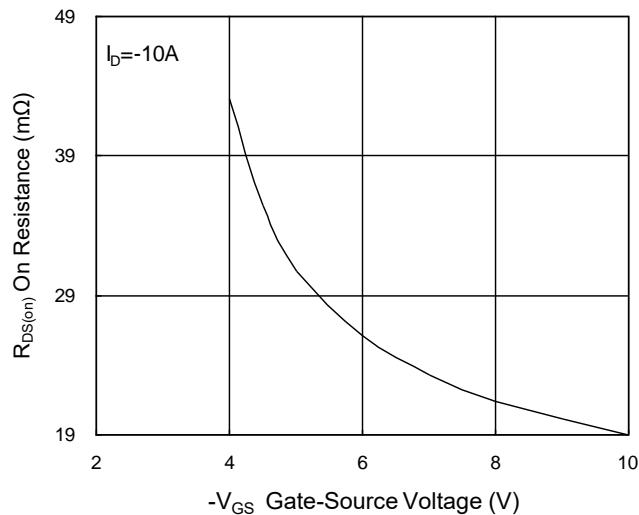
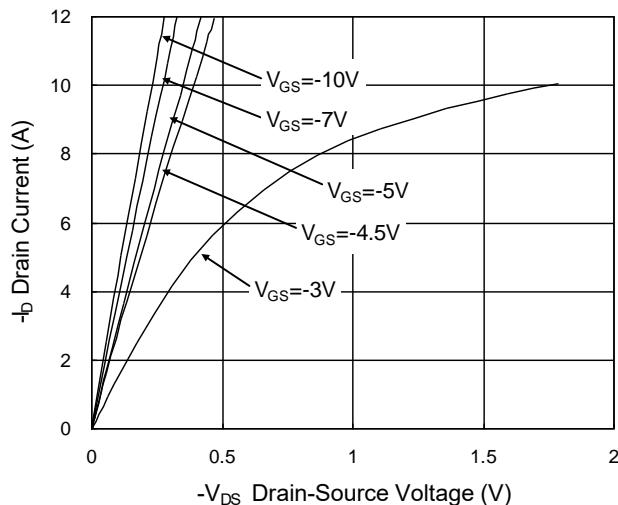
- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The E_{AS} data shows Max. rating . The test condition is $V_{\text{DD}}=-24\text{V}, V_{\text{GS}}=-10\text{V}, L=0.1\text{mH}, I_{\text{AS}}=-17\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

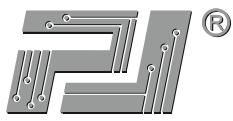


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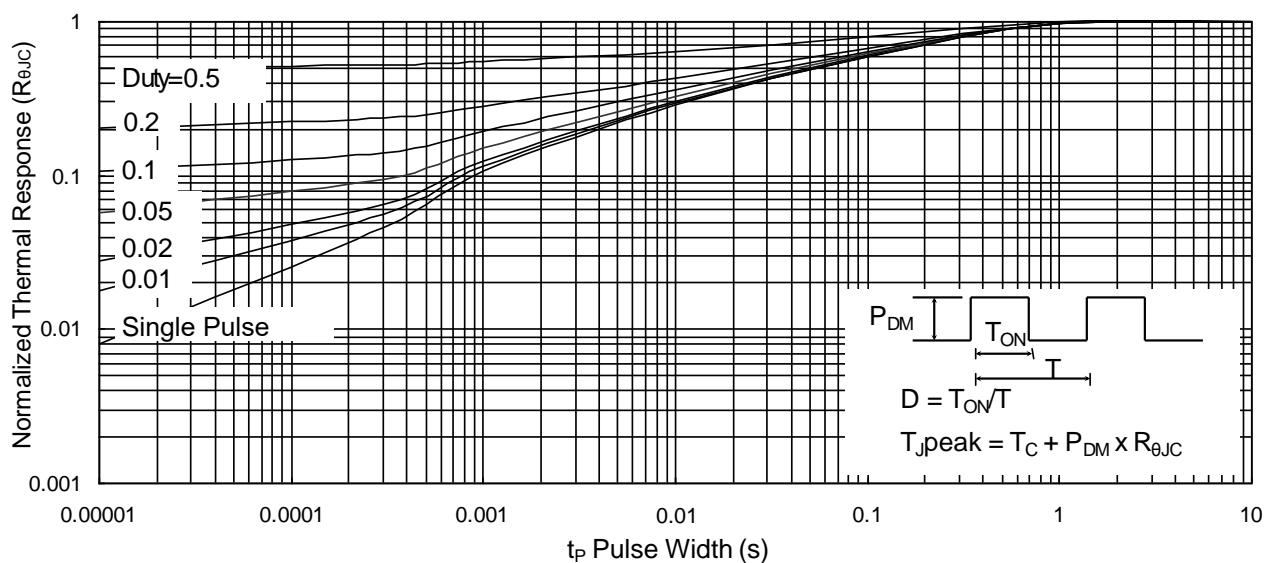
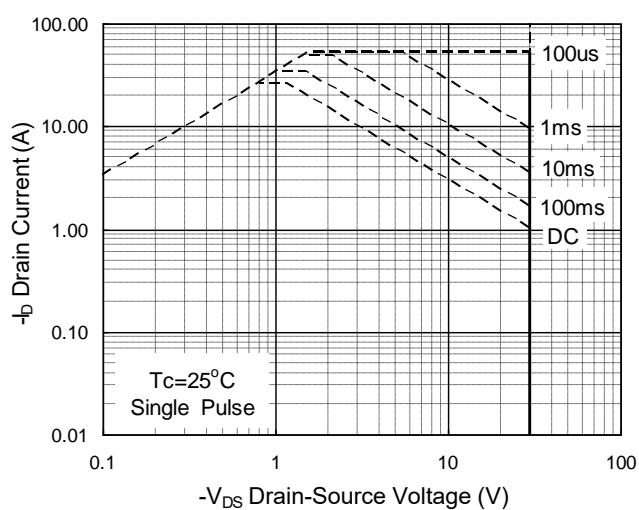
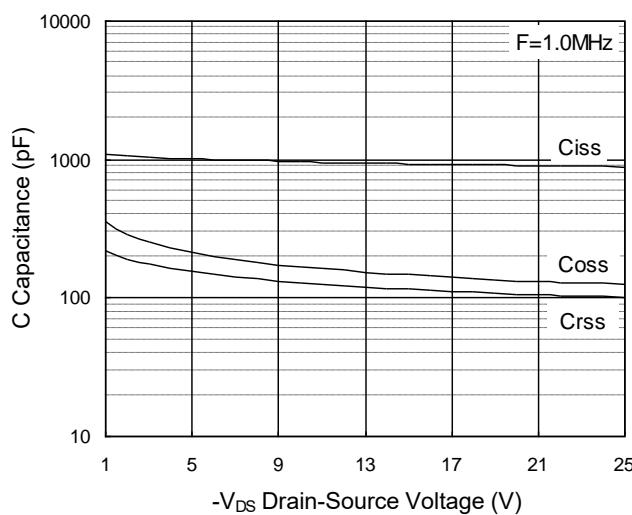
Typical Characteristic Curves





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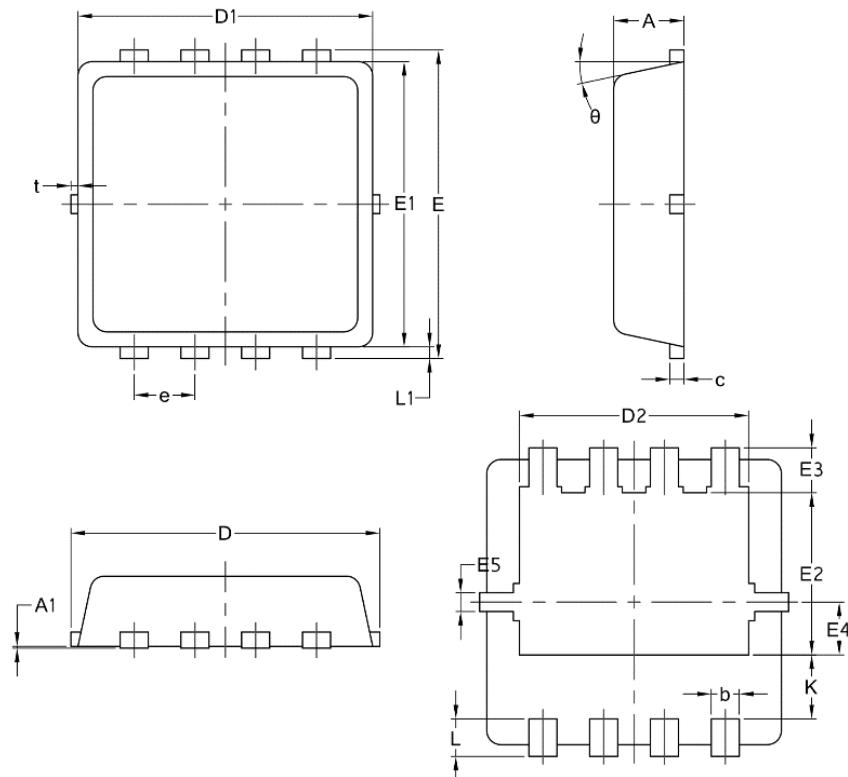




Package Outline

PDFN3x3-8L

Dimensions in mm



Symbol	Common mm		
	Mim	Nom	Max
A	0.70	0.75	0.85
A1	/	/	0.05
b	0.20	0.30	0.40
c	0.10	0.152	0.25
D	3.15	3.30	3.45
D1	3.00	3.15	3.25
D2	2.29	2.45	2.65
E	3.15	3.30	3.45
E1	2.90	3.05	3.20
E2	1.54	1.74	1.94
E3	0.28	0.48	0.65
E4	0.37	0.57	0.77
E5	0.10	0.20	0.30
e	0.60	0.65	0.70
K	0.59	0.69	0.89
L	0.30	0.40	0.50
L1	0.06	0.125	0.20
t	0	0.075	0.13
Φ	10	12	14