



PJM150N60TF

N-Channel Enhancement Mode Power MOSFET

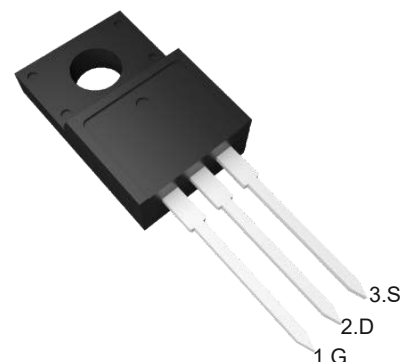
Features

- Fast Switching
- Low Reverse transfer capacitances
- Low gate charge and low $R_{DS(on)}$
- $V_{DS} = 60V, I_D = 150A$
 $R_{DS(on)} < 4.8m\Omega @ V_{GS} = 10V$

Applications

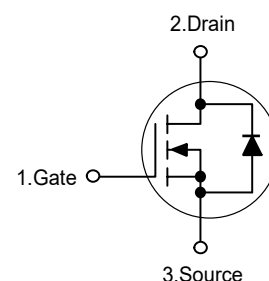
- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

TO-220F



1.Gate 2.Drain 3.Source

Schematic diagram



Absolute Maximum Ratings

Ratings at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	150	A
Drain Current-Pulsed ^{Note1}	I_{DM}	600	A
Single Pulse Avalanche Energy ^{Note1}	E_{AS}	1400	mJ
Maximum Power Dissipation	P_D	140	W
Junction Temperature	T_J	175	°C
Storage Temperature Range	T_{STG}	-55 to +175	°C
Maximum Temperature For Soldering	T_L	300	°C

Thermal Characteristics

Maximum Junction-to-Case ^{Note2}	$R_{\theta JC}$	0.89	°C/W
Maximum Junction-to-Ambient ^{Note2}	$R_{\theta JA}$	60	°C/W



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Electrical Characteristics

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	60	--	--	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V$	--	--	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	--	--	± 1	μA
Gate Threshold Voltage ^{Note3}	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	--	4.0	V
Drain-Source On-Resistance ^{Note3}	$R_{DS(on)}$	$V_{GS}=10V, I_D=75A$	--	3.5	4.8	m Ω
Forward Transconductance ^{Note3}	g_{FS}	$V_{DS}=5V, I_D=75A$	180	--	--	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=30V, V_{GS}=0V, f=1MHz$	--	6500	--	pF
Output Capacitance	C_{oss}		--	650	--	pF
Reverse Transfer Capacitance	C_{rss}		--	600	--	pF
Switching Characteristics						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=30V, V_{GS}=10V$ $I_D=30A, R_G=2.5\Omega$	--	26	--	nS
Turn-on Rise Time	t_r		--	25	--	nS
Turn-off Delay Time	$t_{d(off)}$		--	90	--	nS
Turn-off Fall Time	t_f		--	40	--	nS
Total Gate Charge	Q_g	$V_{DD}=30V, V_{GS}=10V, I_D=30A$	--	165	--	nC
Gate-Source Charge	Q_{gs}		--	30	--	nC
Gate-Drain Charge	Q_{gd}		--	65	--	nC
Source-Drain Diode Characteristics						
Diode Forward Voltage ^{Note3}	V_{SD}	$V_{GS}=0V, I_S=50A$	--	--	1.5	V
Diode Forward Current ^{Note2}	I_{SD}		--	--	150	A

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \leq 10$ sec.

3. Pulse Test: Pulse width $< 380\mu s$, duty cycle $< 2\%$.

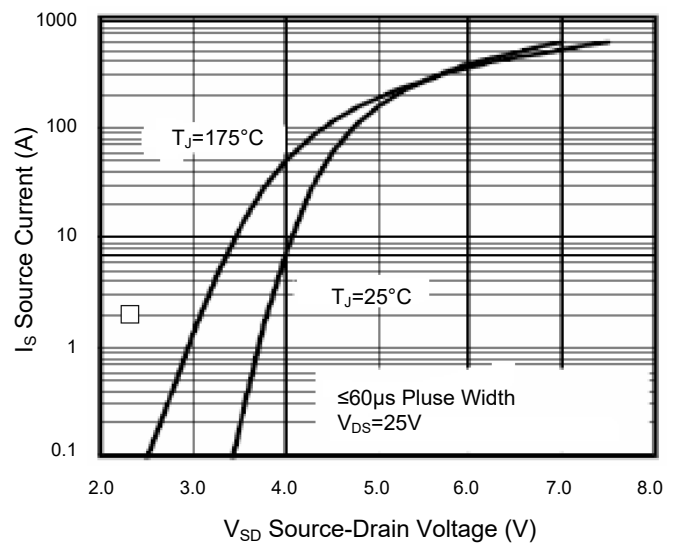
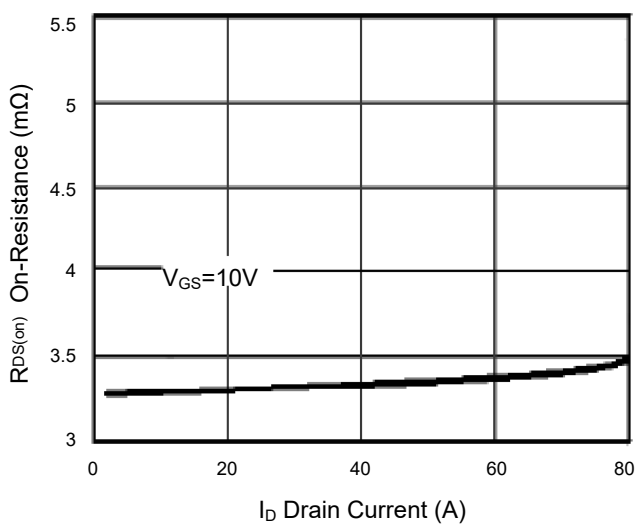
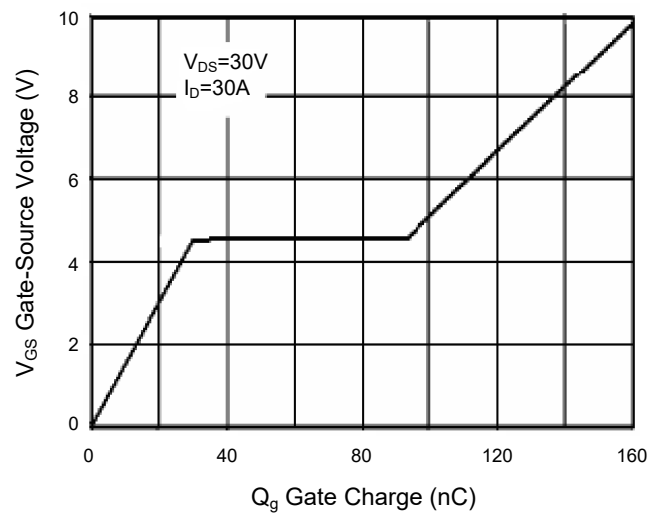
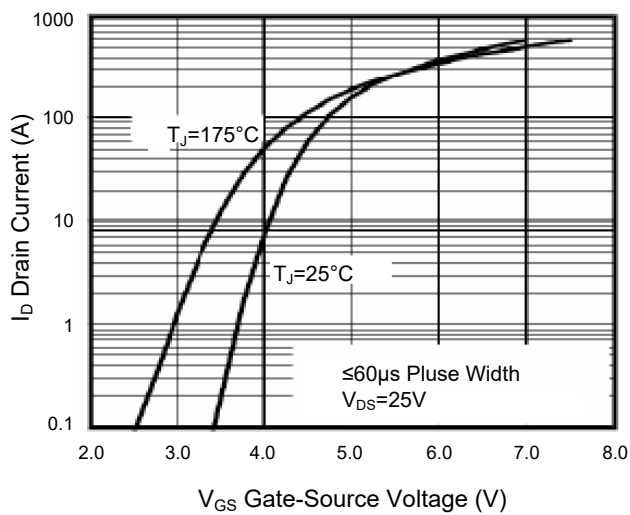
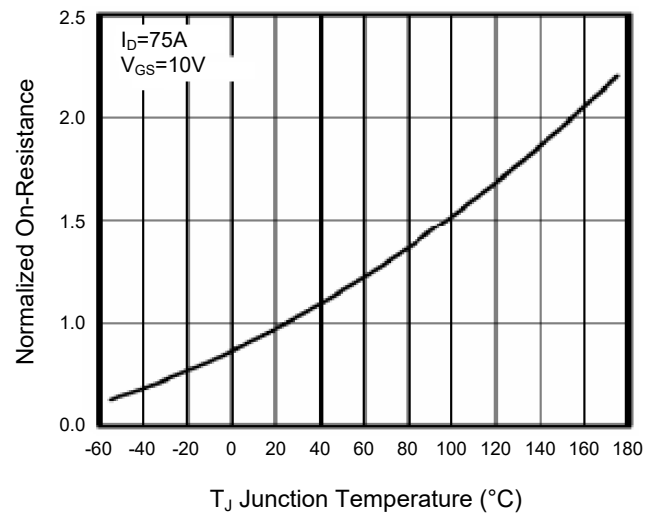
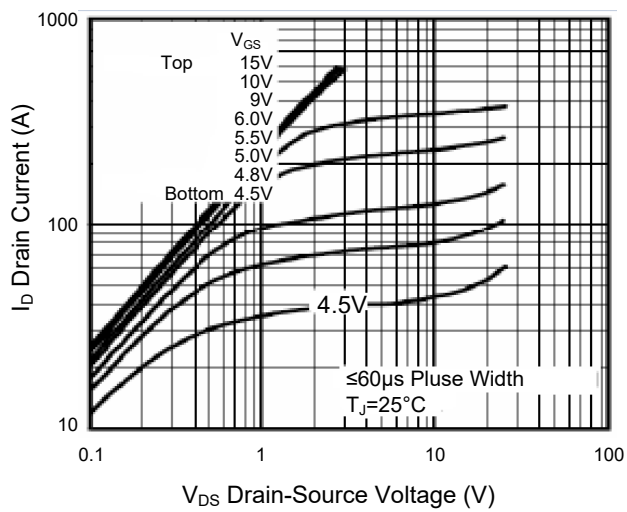
4. E_{AS} Condition: $L=0.5mH, V_{DD}=30V, V_{GS}=10V, R_G=25\Omega$, start $T_J=25^\circ C$.



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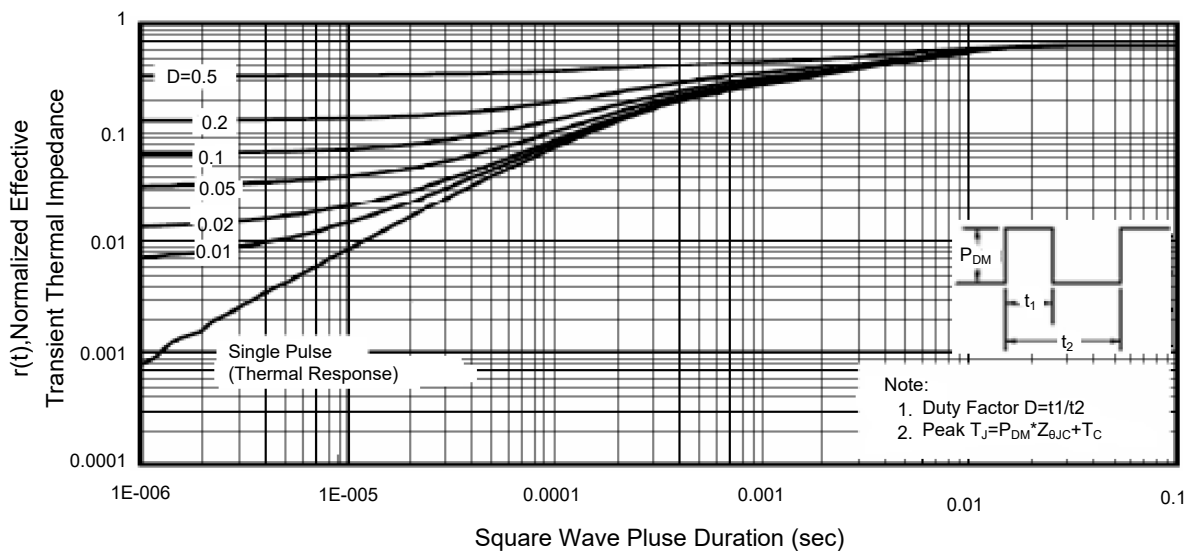
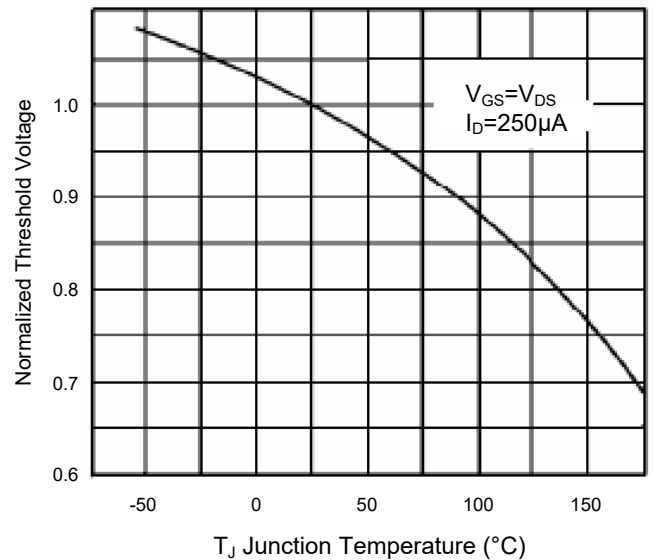
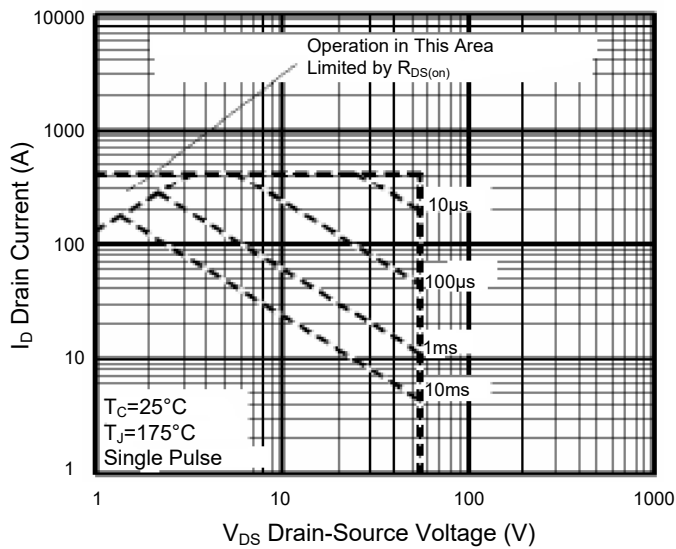
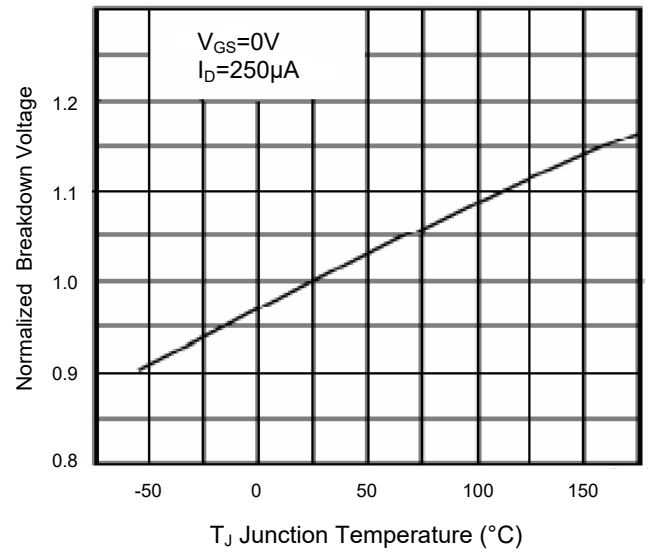
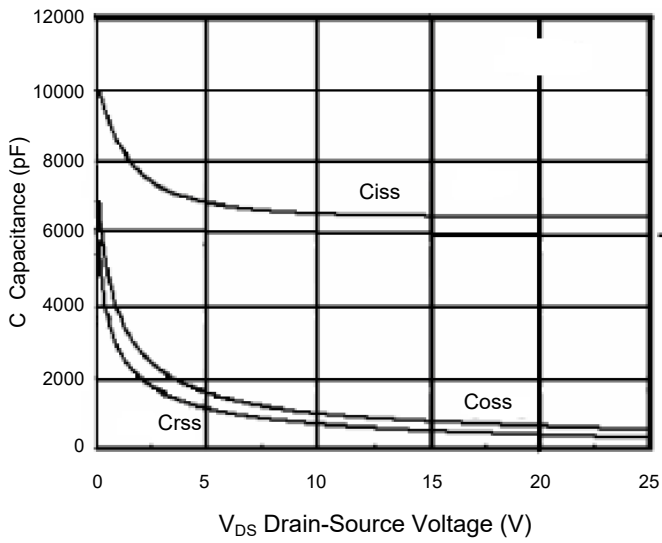
Typical Characteristic Curves





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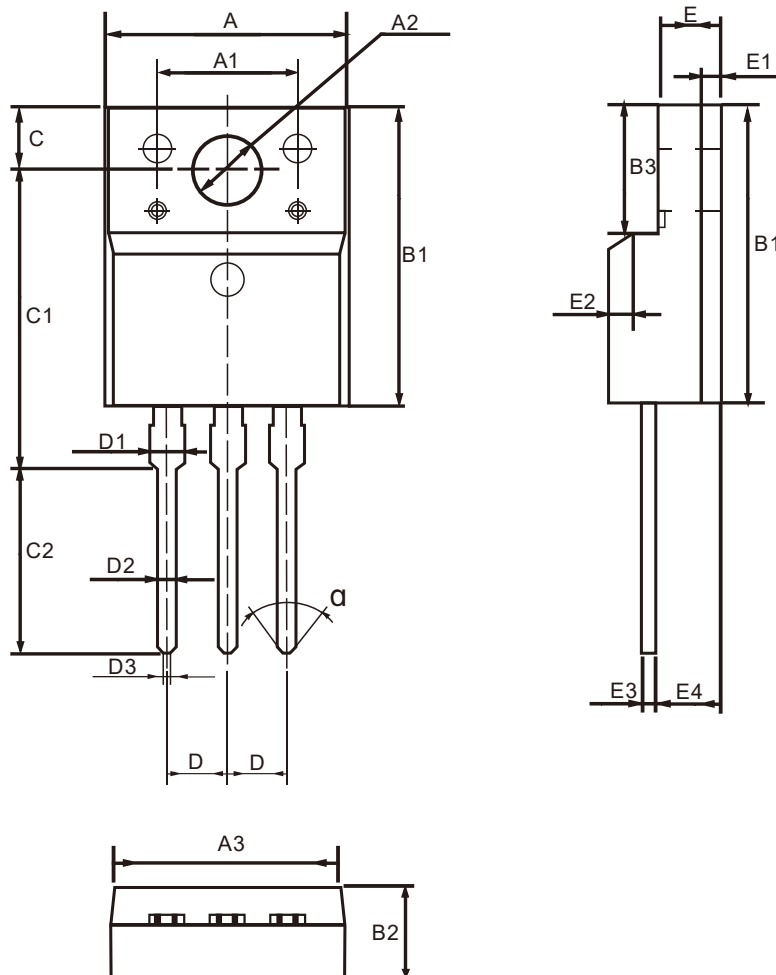
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Package Outline

TO-220F

Dimensions in mm



TO-220F Package Dimensions

UNIT : mm

SYMBOL	min	nom	max	SYMBOL	min	nom	max
A	9.80		10.60	D		2.54	
A1		7.00		D1	1.15		1.55
A2	2.90		3.40	D2	0.60		1.00
A3	9.10		9.90	D3	0.20		0.50
B1	15.40		16.40	E	2.24		2.84
B2	4.35		4.95	E1		0.70	
B3	6.00		7.40	E2		1.0×45°	
C	3.00		3.70	E3	0.35		0.65
C1	15.00		17.00	E4	2.30		3.30
C2	8.80		10.80	α (度)		30°	