

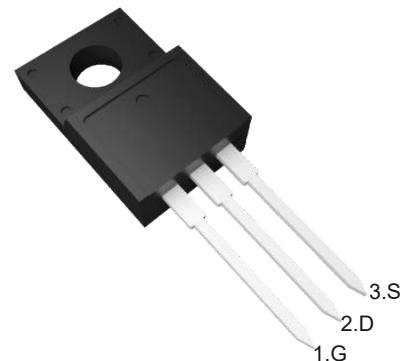
# PJM10H40NTF

## N-Channel Enhancement Mode Power MOSFET

### Features

- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation
- Low gate charge and low  $R_{DS(on)}$
- $V_{DS} = 100V, I_D = 40A$
- $R_{DS(on)} < 17m\Omega @ V_{GS} = 10V$

TO-220F

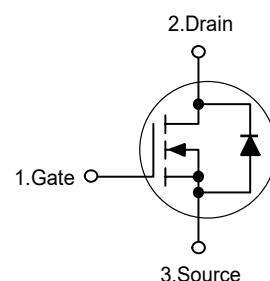


### Applications

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

1.Gate 2.Drain 3.Source

### Schematic diagram



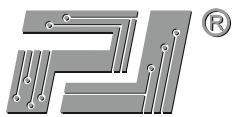
### Absolute Maximum Ratings

Ratings at 25°C case temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	40	A
Drain Current-Pulsed <sup>Note1</sup>	$I_{DM}$	160	A
Single pulse avalanche energy <sup>Note4</sup>	$E_{AS}$	520	mJ
Maximum Power Dissipation	$P_D$	100	W
Junction Temperature	$T_J$	175	°C
Storage Temperature Range	$T_{STG}$	-55 to +175	°C

### Thermal Characteristics

Maximum Junction-to-Case <sup>Note2</sup>	$R_{eJC}$	0.783	°C/W
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### Electrical Characteristics

( $T_C=25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	100	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{DS}=100\text{V}, V_{GS}=0\text{V}$	--	--	0.1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	--	--	$\pm 10$	$\mu\text{A}$
Gate Threshold Voltage <sup>Note3</sup>	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0	--	2.5	V
Drain-Source On-Resistance <sup>Note3</sup>	$R_{DS(\text{on})}$	$V_{GS}=10\text{V}, I_D=20\text{A}$	--	14	17	$\text{m}\Omega$
Forward Transconductance <sup>Note3</sup>	$g_{FS}$	$V_{DS}=25\text{V}, I_D=28\text{A}$	32	--	--	S
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=25\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	1133	--	pF
Output Capacitance	$C_{oss}$		--	70	--	pF
Reverse Transfer Capacitance	$C_{rss}$		--	71	--	pF
<b>Switching Characteristics</b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=30\text{V}, R_L=15\Omega$ $I_D=2\text{A}, V_{GS}=10\text{V}, R_G=2.5\Omega$	--	5	--	nS
Turn-on Rise Time	$t_r$		--	4	--	nS
Turn-off Delay Time	$t_{d(off)}$		--	16	--	nS
Turn-off Fall Time	$t_f$		--	5	--	nS
Total Gate Charge	$Q_g$	$V_{DD}=30\text{V}, I_D=30\text{A}, V_{GS}=10\text{V}$	--	23	--	nC
Gate-Source Charge	$Q_{gs}$		--	5	--	nC
Gate-Drain Charge	$Q_{gd}$		--	6.5	--	nC
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>Note3</sup>	$V_{SD}$	$V_{GS}=0\text{V}, I_S=28\text{A}$	--	--	1.2	V
Diode Forward Current <sup>Note2</sup>	$I_S$		--	--	40	A

Note: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.

3. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .

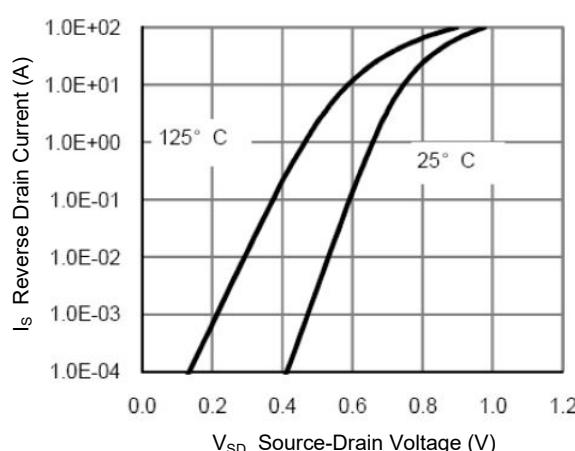
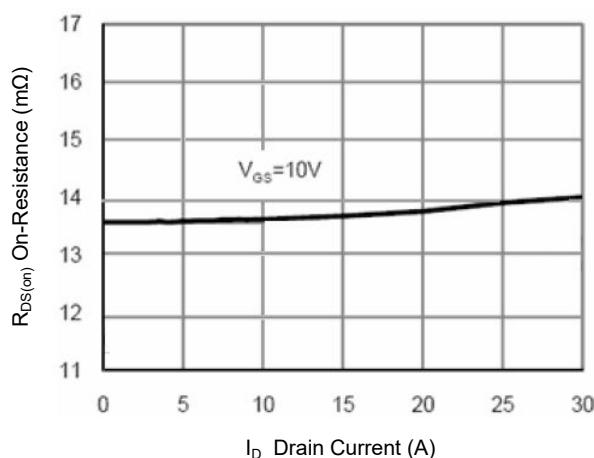
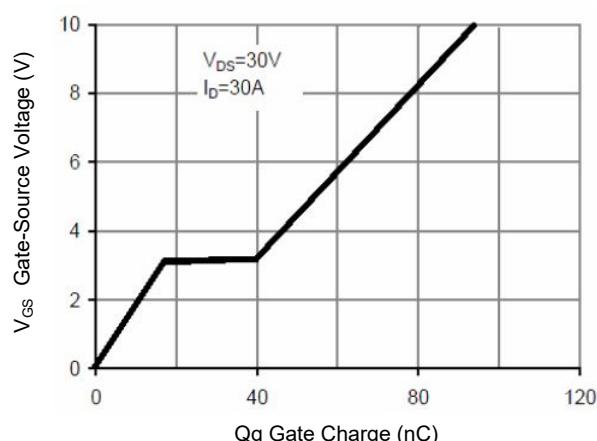
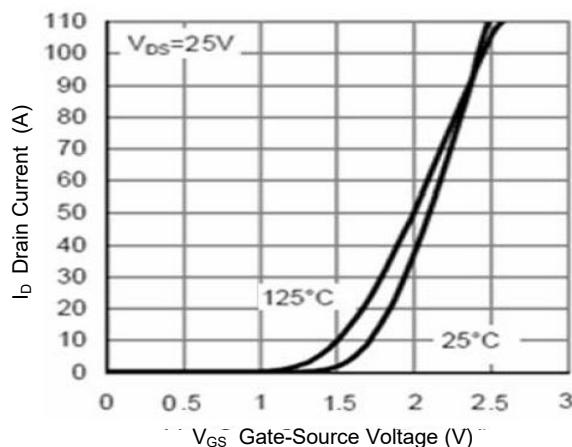
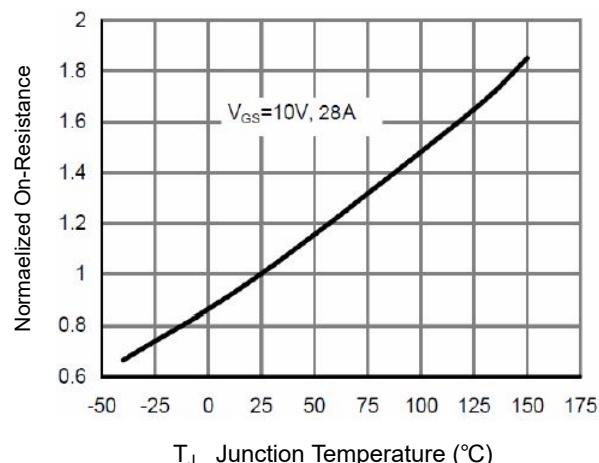
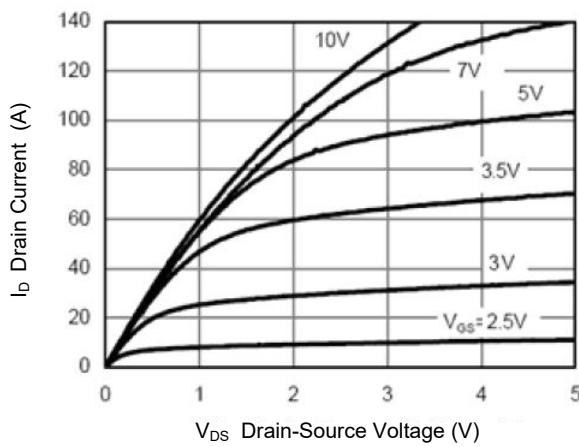
4.  $E_{AS}$  Condition:  $V_{DD}=50\text{V}, V_{GS}=10\text{V}, L=0.5\text{mH}, R_G=25\Omega$ , Start  $T_J=25^\circ\text{C}$ .



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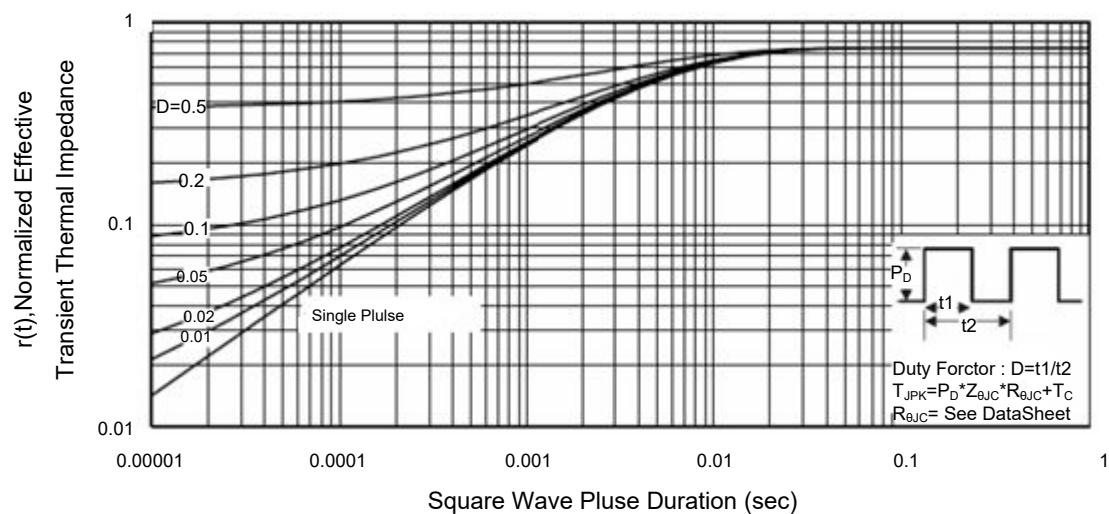
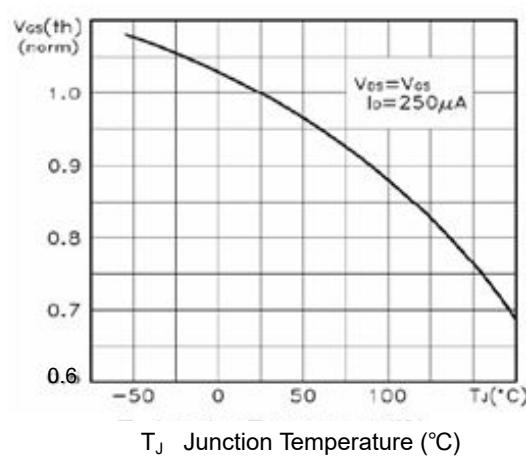
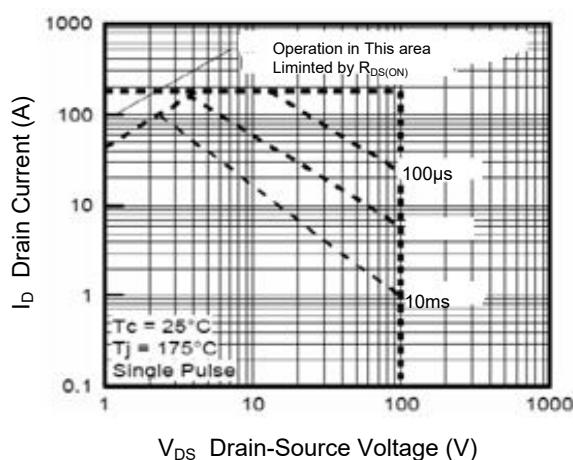
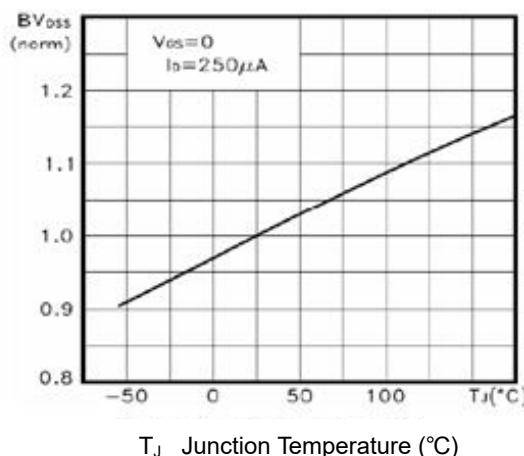
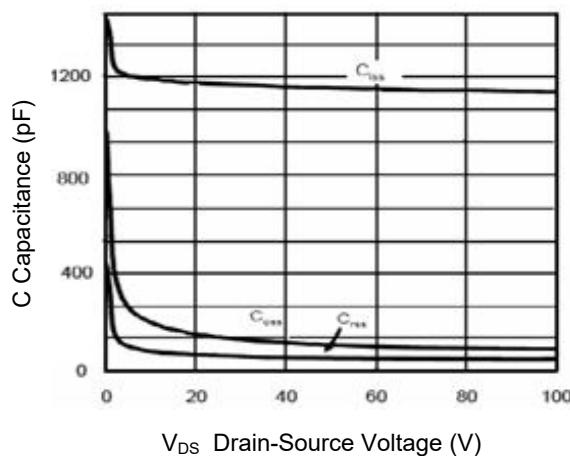
### Typical Characteristic Curves





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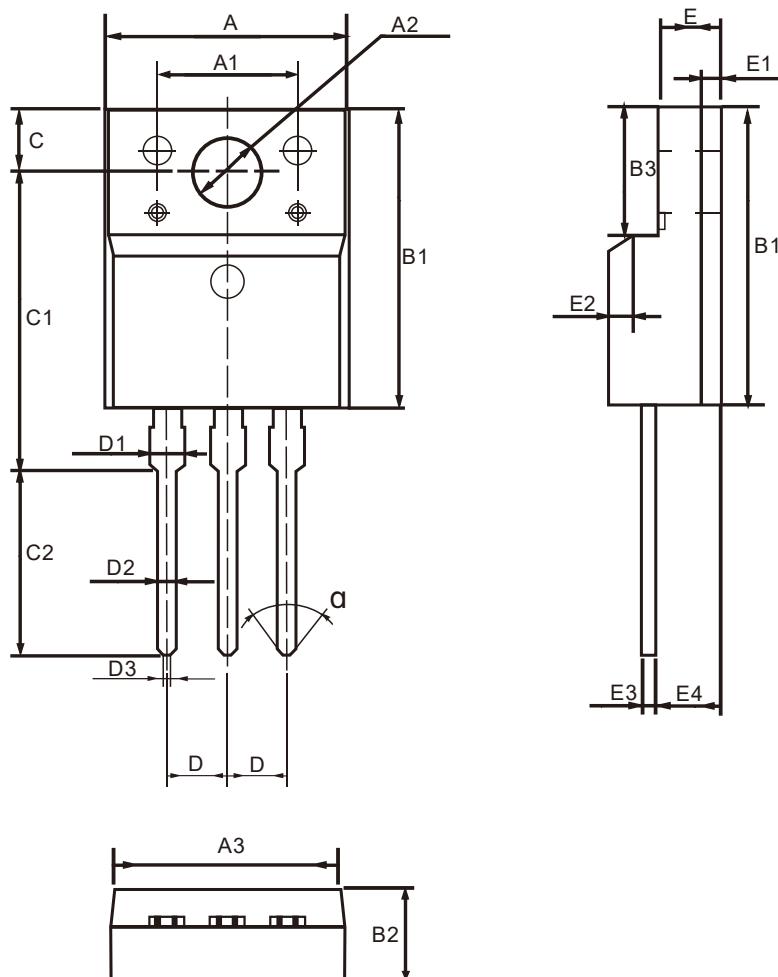




## Package Outline

TO-220F

Dimensions in mm



TO-220F Package Dimensions

UNIT : mm

SYMBOL	min	nom	max	SYMBOL	min	nom	max
A	9.80		10.60	D		2.54	
A1		7.00		D1	1.15		1.55
A2	2.90		3.40	D2	0.60		1.00
A3	9.10		9.90	D3	0.20		0.50
B1	15.40		16.40	E	2.24		2.84
B2	4.35		4.95	E1		0.70	
B3	6.00		7.40	E2		1.0 × 45°	
C	3.00		3.70	E3	0.35		0.65
C1	15.00		17.00	E4	2.30		3.30
C2	8.80		10.80	α (度)		30°	