

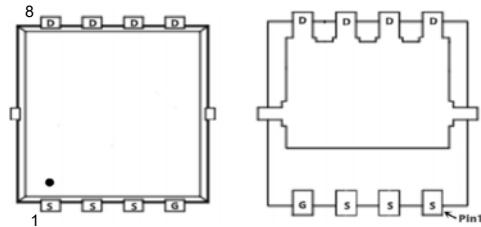
PJM10H100NDN

N-Channel Enhancement Mode Power MOSFET

Features

- Extremely low switching loss
- $V_{DS}=100V, I_D= 100A$
- $R_{DS(on)} < 8.0m\Omega @ V_{GS} = 10V$

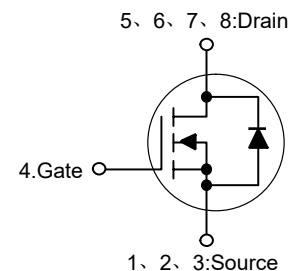
PDFN5x6-8L



Schematic Diagram

Applications

- Consumer electronic power supply
- Motor control
- Synchronous-rectification
- Synchronous-rectification applications



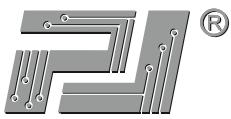
Absolute Maximum Ratings

Ratings at 25°C junction temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous ($T_c=25^\circ C$) at $V_{GS}=10V$ ^{Note1}	I_D	100	A
Drain Current-Pulsed ($T_c=25^\circ C$) ^{Note2}	I_{DM}	300	A
Single Pulse Avalanche Energy ^{Note3}	E_{AS}	130	mJ
Maximum Power Dissipation ($T_c=25^\circ C$) ^{Note4}	P_D	148	W
Junction Temperature	T_J	150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

Thermal Characteristics

Thermal Resistance, Junction-to-Ambient ^{Note5}	$R_{\theta JA}$	62	°C/W
Maximum Junction-to-Case	$R_{\theta JC}$	0.84	°C/W



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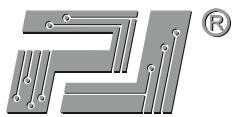
Electrical Characteristics

($T_J=25^\circ\text{C}$ unless otherwise specified)

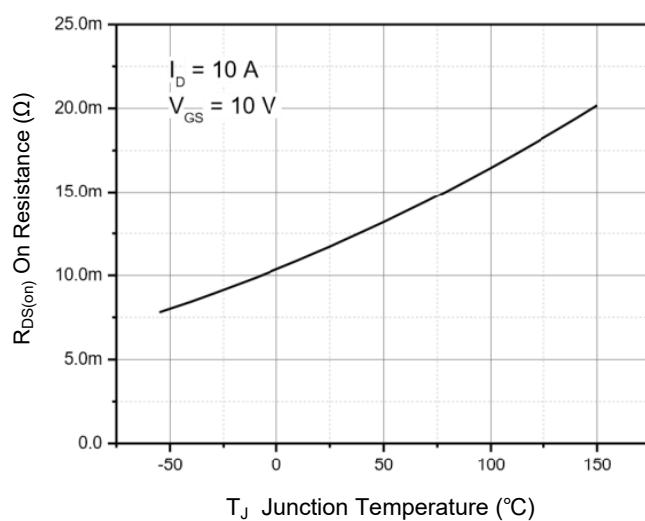
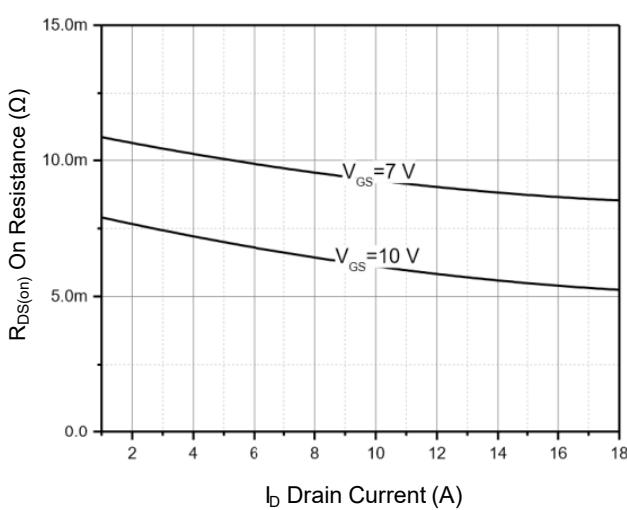
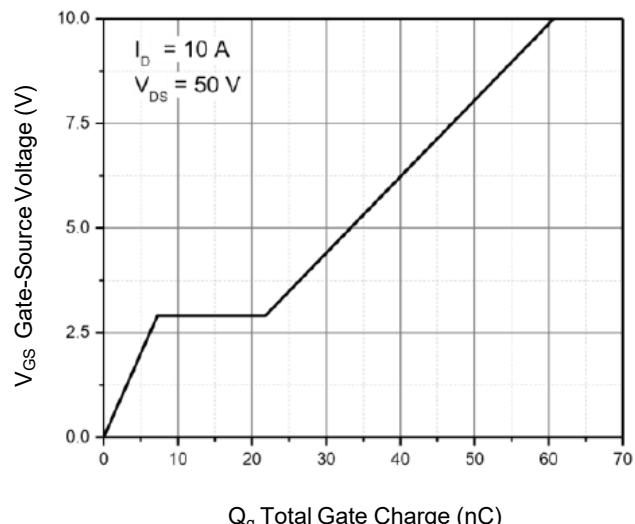
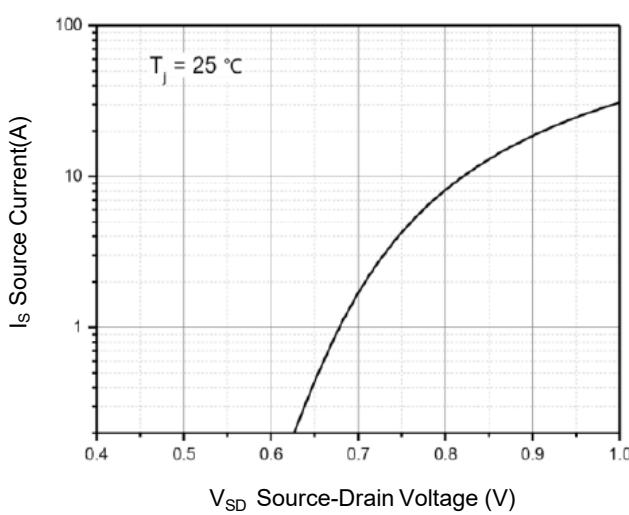
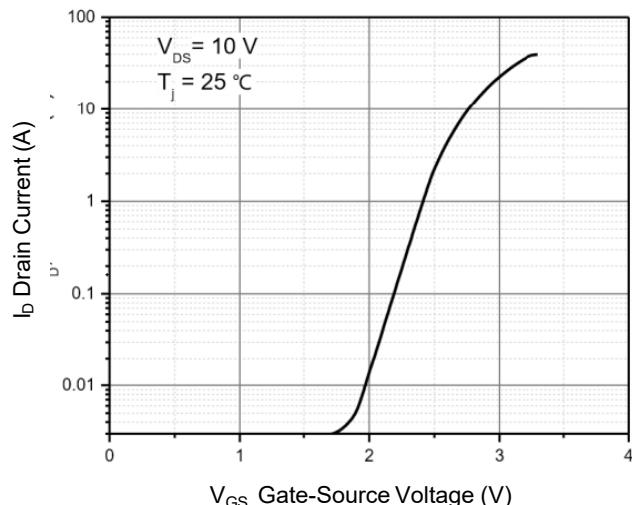
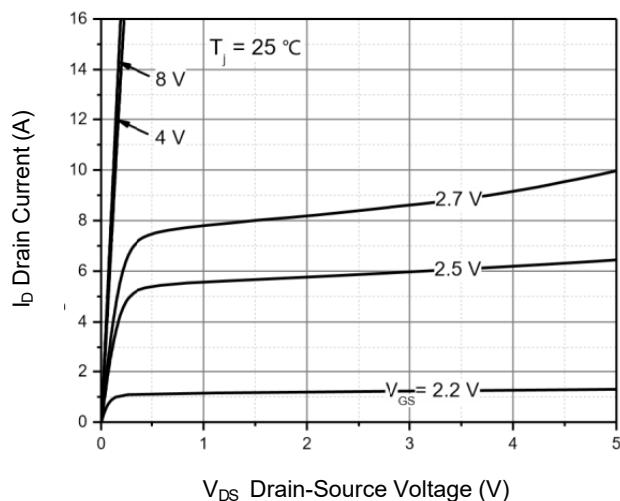
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	100	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.0	--	4.0	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=12\text{A}$	--	--	8.0	$\text{m}\Omega$
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}}=50\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	3530	--	pF
Output Capacitance	C_{oss}		--	560.1	--	pF
Reverse Transfer Capacitance	C_{rss}		--	9	--	pF
Switching Characteristics						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=10\text{A}$ $V_{\text{GS}}=10\text{V}, R_{\text{G}}=2\Omega$	--	22.5	--	nS
Turn-on Rise Time	t_r		--	8.6	--	nS
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	66.6	--	nS
Turn-off Fall Time	t_f		--	42.1	--	nS
Total Gate Charge	Q_g	$V_{\text{DS}}=50\text{V}, I_{\text{D}}=10\text{A}, V_{\text{GS}}=10\text{V}$	--	60.7	--	nC
Gate-Source Charge	Q_{gs}		--	7.2	--	nC
Gate-Drain Charge	Q_{gd}		--	14.6	--	nC
Source-Drain Diode Characteristics						
Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=30\text{A}$	--	--	1.3	V
Diode Forward Current	I_{s}		--	--	100	A

Note :

- 1.Calculated continuous current based on maximum allowable junction temperature.
- 2.Repetitive rating; pulse width limited by max. junction temperature.
- 3.The E_{AS} data shows Max. rating . The test condition is $V_{\text{DD}}=50\text{V}, R_{\text{G}}=50 \Omega, L=0.3 \text{ mH}$, starting $T_J=25^\circ\text{C}$
4. P_D is based on max. junction temperature, using junction-case thermal resistance.
- 5.The value of $R_{\theta\text{JA}}$ is measured with the device mounted on 1 inch² FR-4 board with 2OZ. Copper, in a still air environment with $T_a=25^\circ\text{C}$



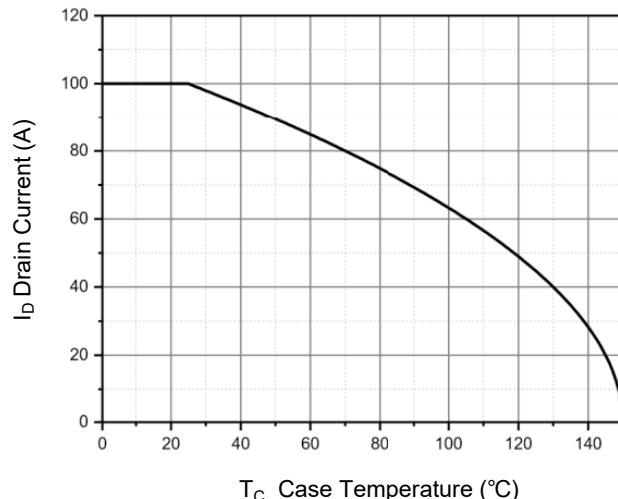
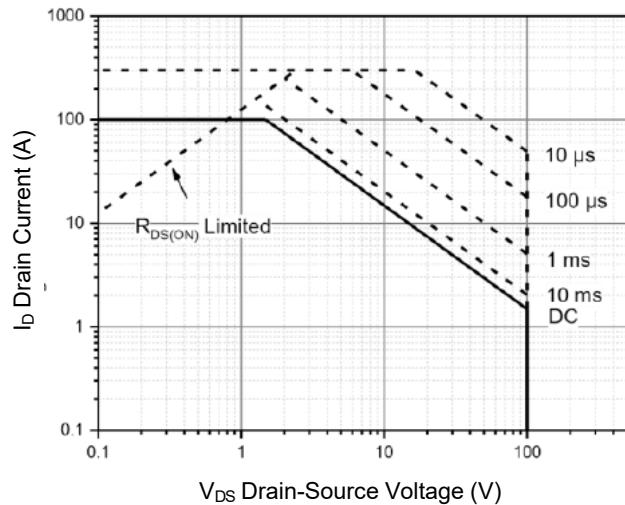
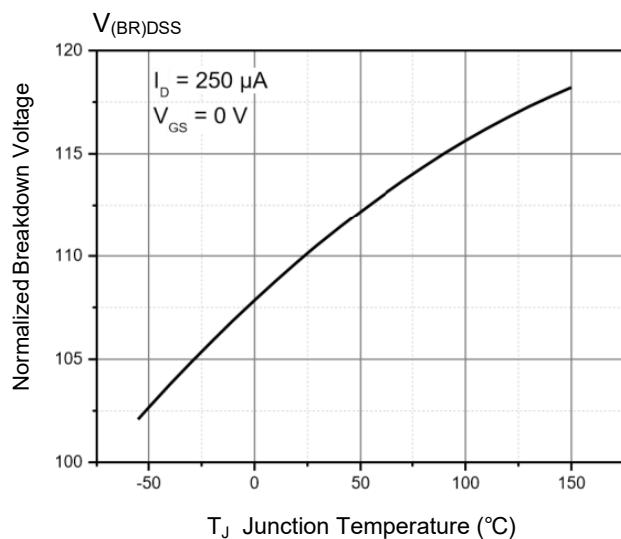
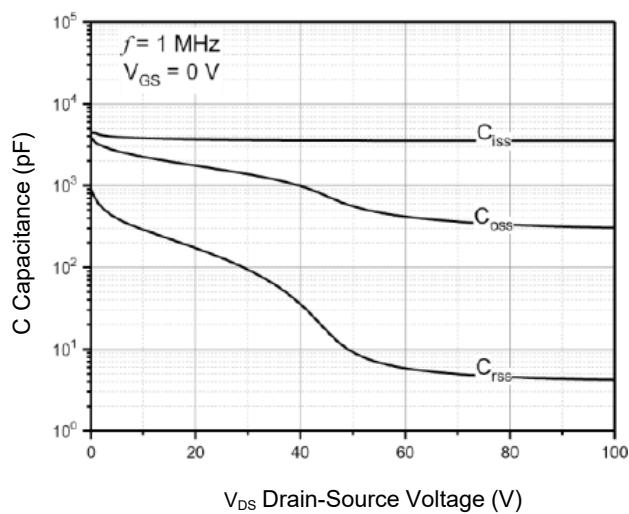
Typical Characteristic Curves

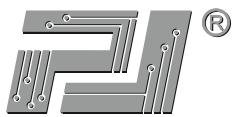




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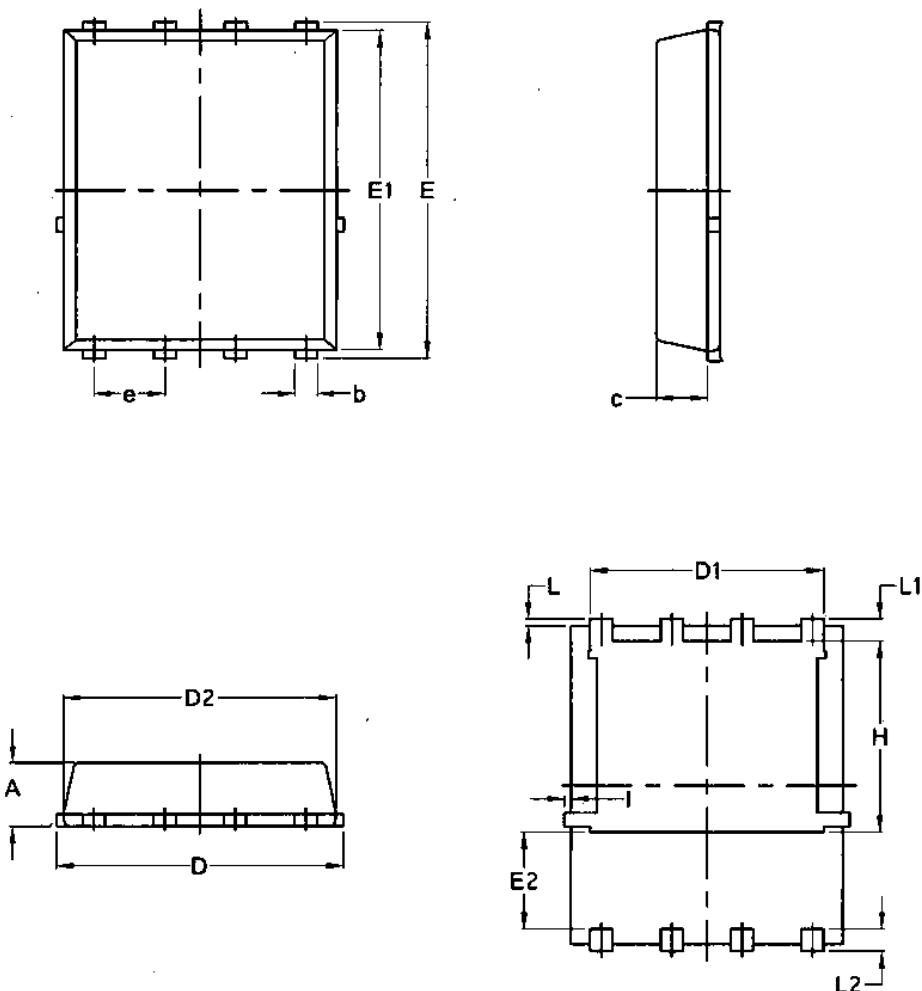
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Package Outline

PDFN5x6-8L

Dimensions in mm



Symbol	Common			
	mm		Inch	
	Min	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070