



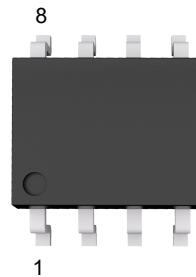
# PJM09DP30PA

## Dual P-Channel Enhancement Mode Power MOSFET

### Features

- High density cell design for ultra low  $R_{DS(on)}$
- Advanced trench technology
- $V_{DS} = -30V, I_D = -9A$
- $R_{DS(on)} < 25m\Omega @ V_{GS} = -10V$

SOP-8

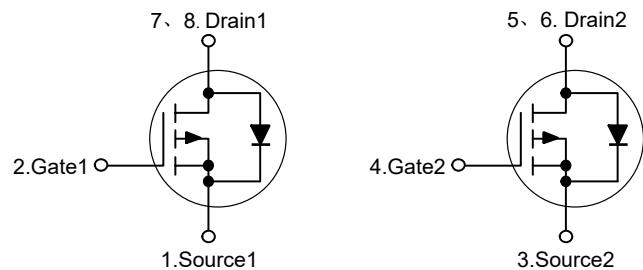


Marking code: 09DP30

### Applications

- PWM applications
- Load switch
- Power management

Schematic Diagram



### Absolute Maximum Ratings

Ratings at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$-V_{DS}$	30	V
Gate-Source Voltage	$-V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$-I_D$	9	A
Drain Current-Pulsed <sup>Note1</sup>	$-I_{DM}$	36	A
Maximum Power Dissipation	$P_D$	3.3	W
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-55 to +150	°C

### Thermal Characteristics

Thermal Resistance, Junction-to-Ambient <sup>Note2</sup>	$R_{\theta JA}$	38	°C/W
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### Electrical Characteristics

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	-V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	30	--	--	V
Zero Gate Voltage Drain Current	-I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	--	--	1	μA
Gate-Body Leakage Current	-I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
Gate Threshold Voltage <sup>Note3</sup>	-V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	1	1.5	2.5	V
Drain-Source On-Resistance <sup>Note3</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-9A	--	19	25	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A	--	27	38	mΩ
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz	--	1200	--	pF
Output Capacitance	C <sub>oss</sub>		--	155	--	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		--	139	--	pF
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-15V, I <sub>D</sub> =-1A V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω	--	13	--	nS
Turn-on Rise Time	t <sub>r</sub>		--	12	--	nS
Turn-off Delay Time	t <sub>d(off)</sub>		--	198	--	nS
Turn-off Fall Time	t <sub>f</sub>		--	98	--	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DD</sub> =-15V, I <sub>D</sub> =-8A V <sub>GS</sub> =-10V	--	52	--	nC
Gate-Source Charge	Q <sub>gs</sub>		--	9.8	--	nC
Gate-Drain Charge	Q <sub>gd</sub>		--	8.3	--	nC
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>Note3</sup>	-V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-9A	--	--	1.2	V
Diode Forward Current <sup>Note2</sup>	-I <sub>S</sub>		--	--	9	A

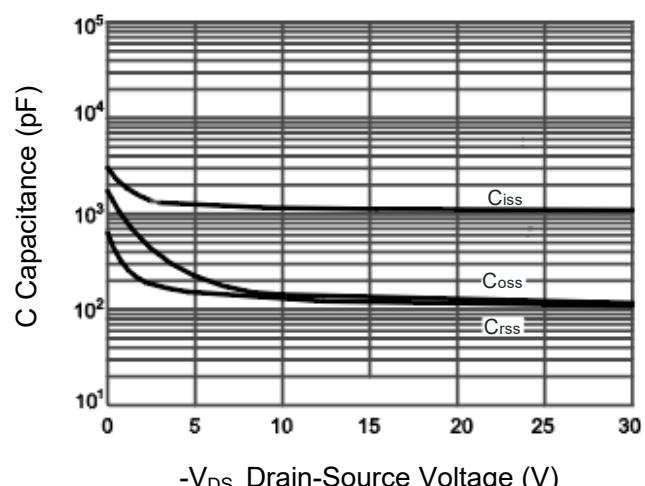
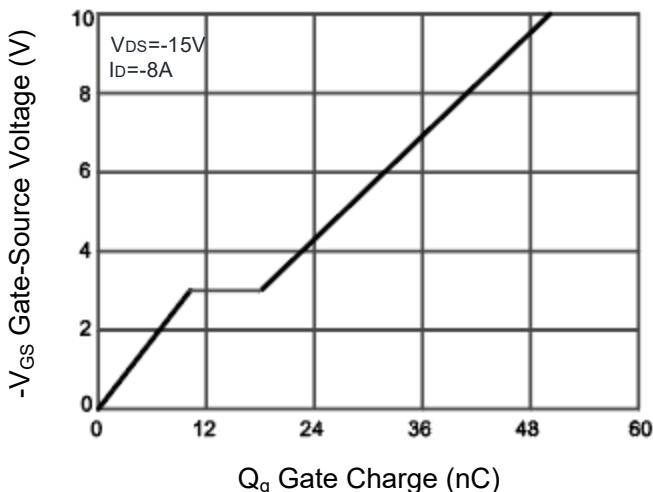
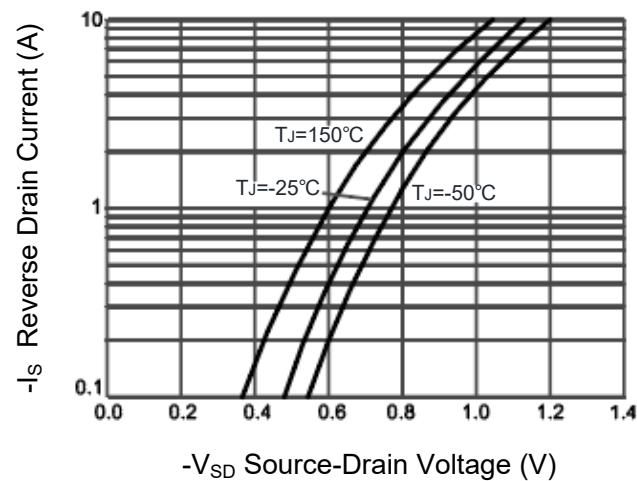
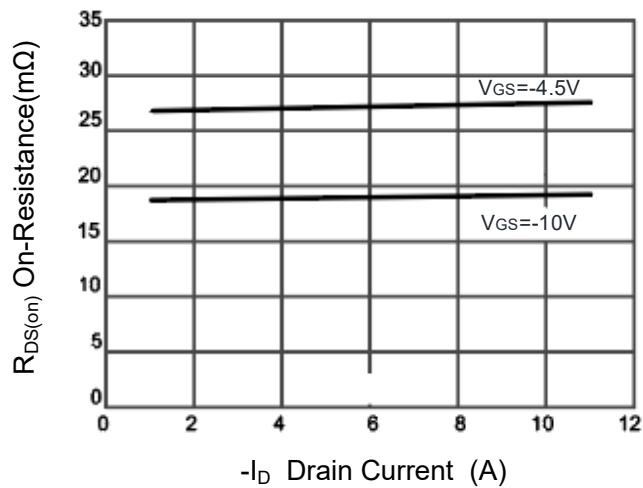
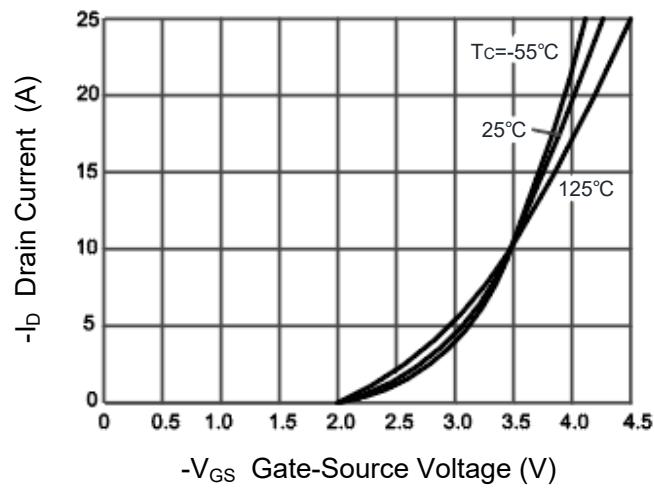
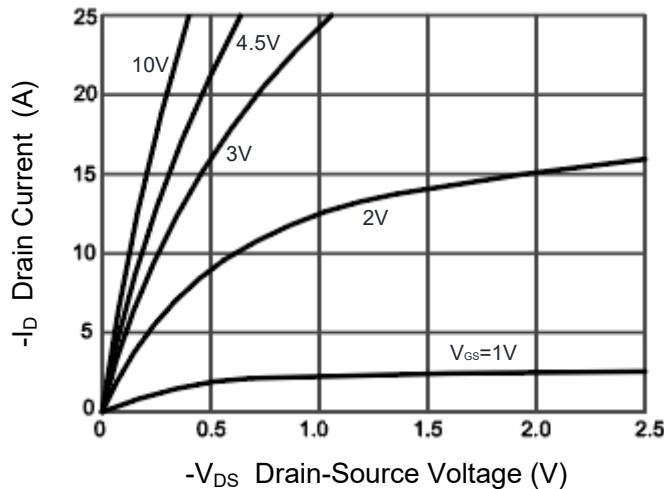
Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

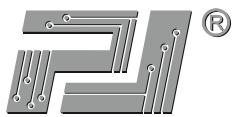
2. Surface Mounted on FR4 Board, t ≤ 10 sec.

3. Pulse Test: Pulse width≤300μs, duty cycle≤2%



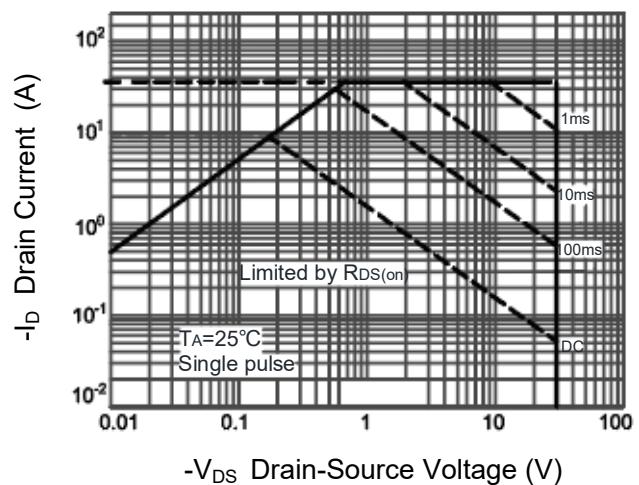
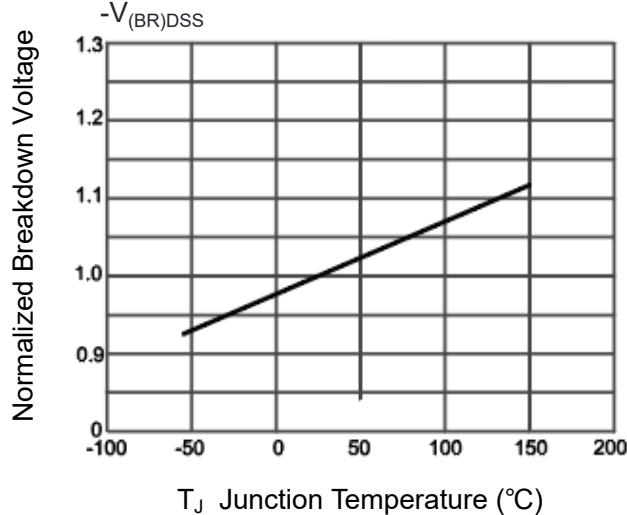
### Typical Characteristic Curves





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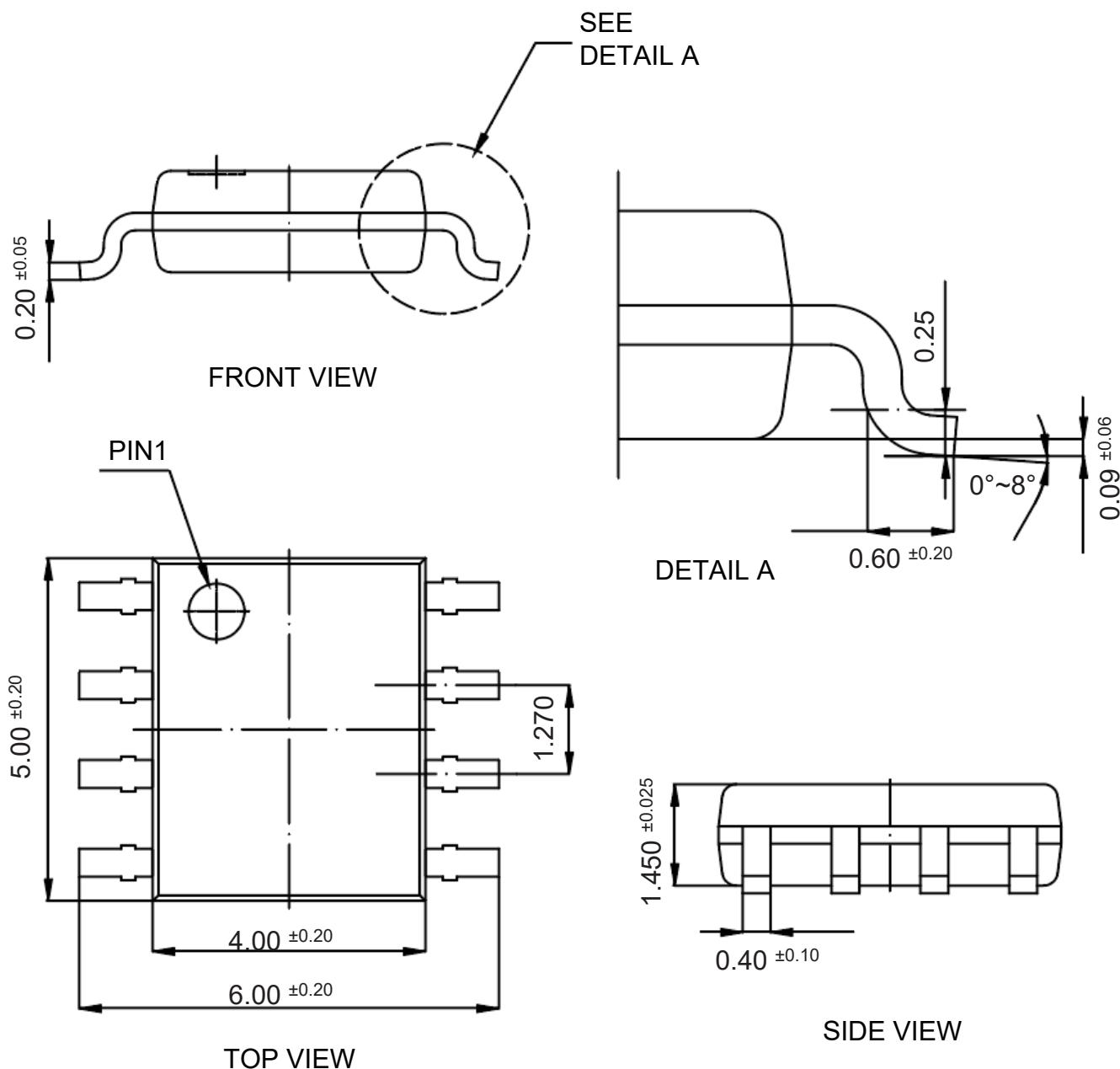




### Package Outline

SOP-8

Dimensions in mm



### Ordering Information

Device	Package	Shipping
PJM09DP30PA	SOP-8	4,000PCS/Reel&13inches