



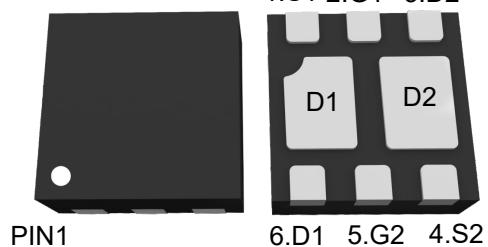
# PJM03DP20DFA

## Dual P-Channel Enhancement Mode Power MOSFET

### Features

- Low gate charge and  $R_{DS(ON)}$
- $V_{DS} = -20V, I_D = -3A$
- $R_{DS(on)} < 110m\Omega @ V_{GS} = -4.5V$

### DFN2x2A-6L

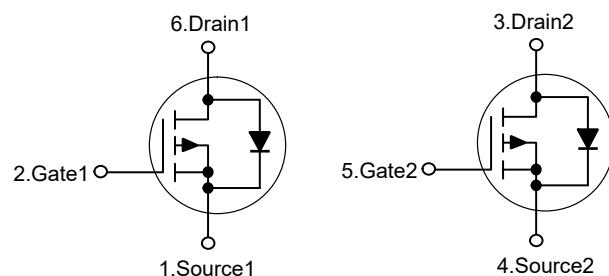


Marking Code: 03DP20

### Applications

- Load switch
- PWM application

### Schematic Diagram



### Absolute Maximum Ratings

Ratings at 25°C Case temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$-V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$-I_D$	3	A
Drain Current-Pulsed <sup>Note1</sup>	$-I_{DM}$	10	A
Maximum Power Dissipation	$P_D$	1.5	W
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-55 to +150	°C

### Thermal Characteristics

Thermal Resistance, Junction-to-Ambient <sup>Note2</sup>	$R_{\theta JA}$	83	°C/W
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### Electrical Characteristics

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	-V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	20	--	--	V
Zero Gate Voltage Drain Current	-I <sub>DSS</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V	--	--	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V	--	--	±100	nA
Gate Threshold Voltage <sup>Note3</sup>	-V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	0.4	0.7	1	V
Drain-Source On-Resistance <sup>Note3</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.8A	--	85	110	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-2A	--	120	140	mΩ
Forward Transconductance <sup>Note3</sup>	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-2A	5	--	--	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V, f=1MHz	--	405	--	pF
Output Capacitance	C <sub>oss</sub>		--	75	--	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		--	55	--	pF
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1A V <sub>GEN</sub> =-4.5V, R <sub>G</sub> =10Ω	--	11	--	nS
Turn-on Rise Time	t <sub>r</sub>		--	15	--	nS
Turn-off Delay Time	t <sub>d(off)</sub>		--	30	--	nS
Turn-off Fall Time	t <sub>f</sub>		--	10	--	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-3A, V <sub>GS</sub> =-2.5V	--	3.3	--	nC
Gate-Source Charge	Q <sub>gs</sub>		--	0.7	--	nC
Gate-Drain Charge	Q <sub>gd</sub>		--	1.3	--	nC
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>Note3</sup>	-V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>s</sub> =-2.5A	--	--	1.2	V
Diode Forward Current <sup>Note2</sup>	-I <sub>s</sub>		--	--	3	A

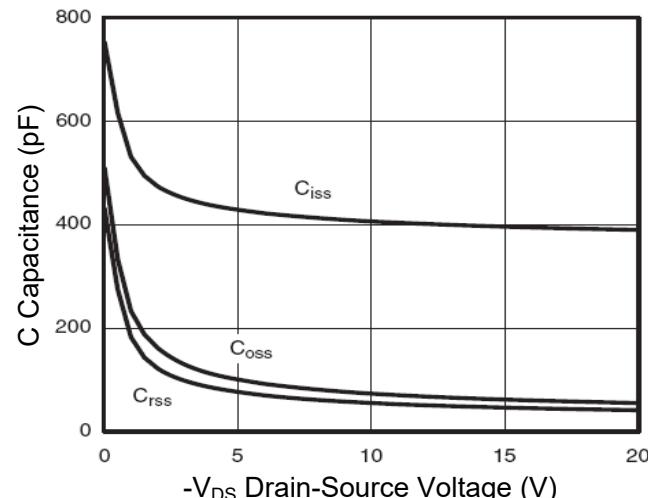
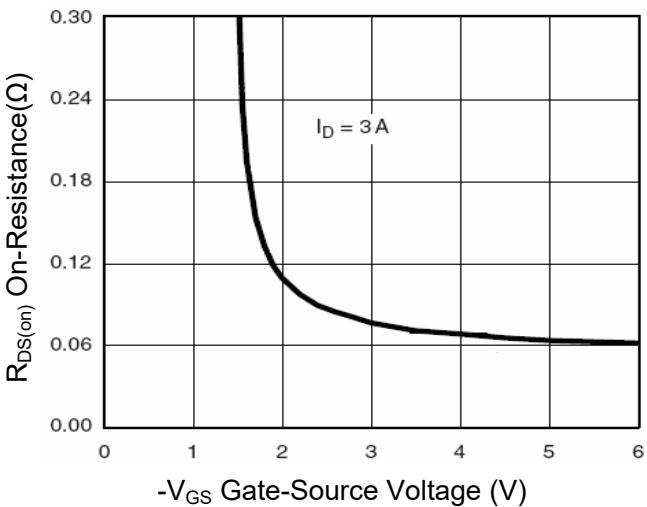
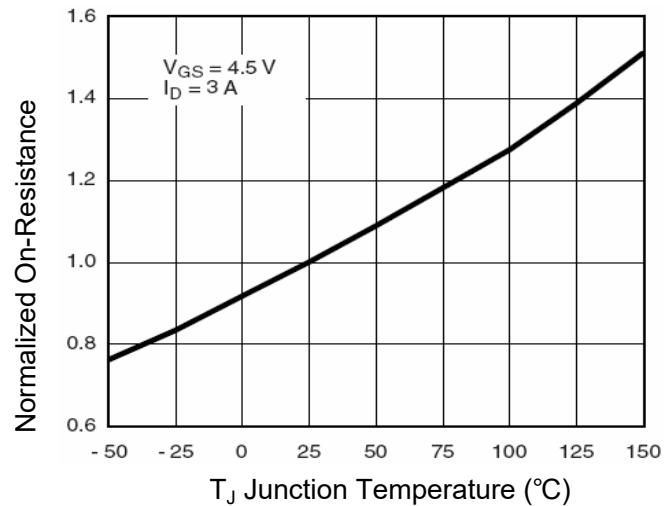
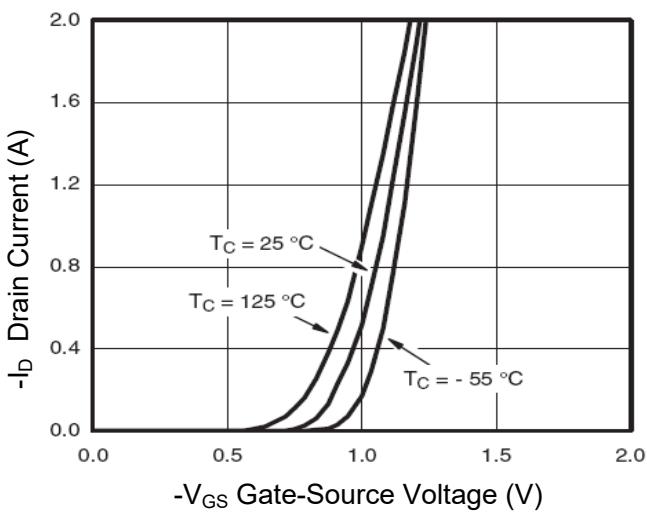
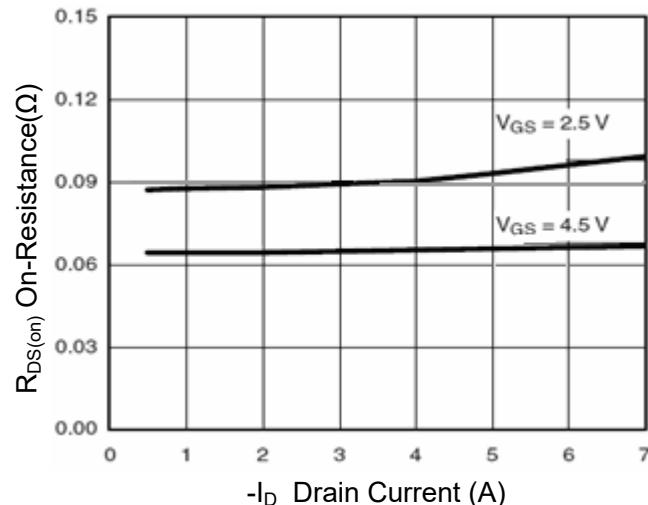
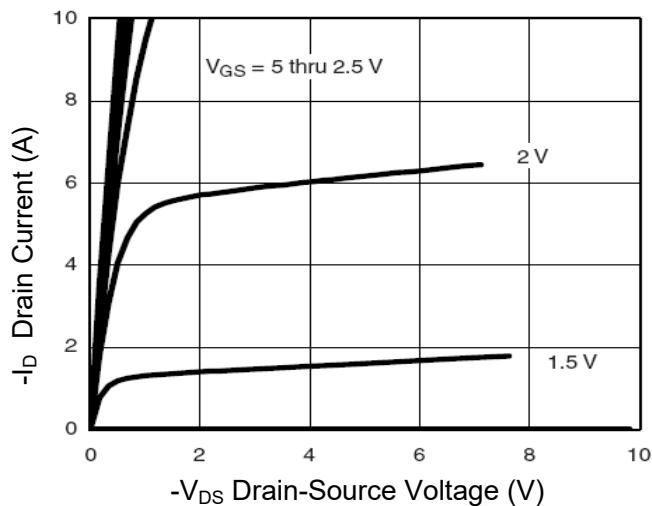
Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

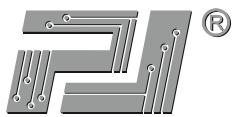
2. Surface Mounted on FR4 Board, t ≤ 10 sec.

3. Pulse Test: Pulse width≤300μs, duty cycle≤2%.



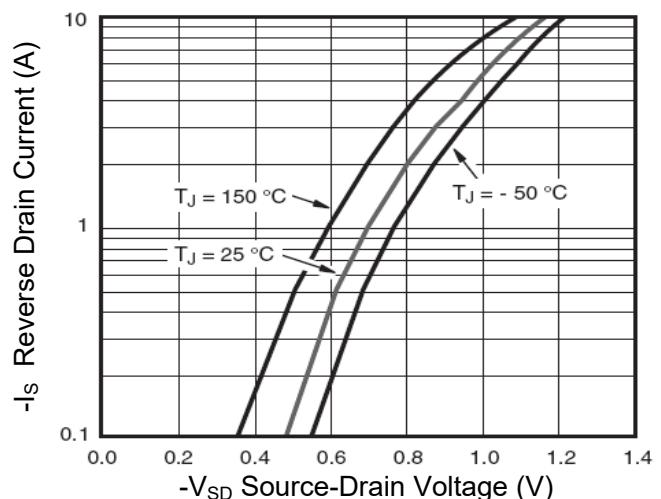
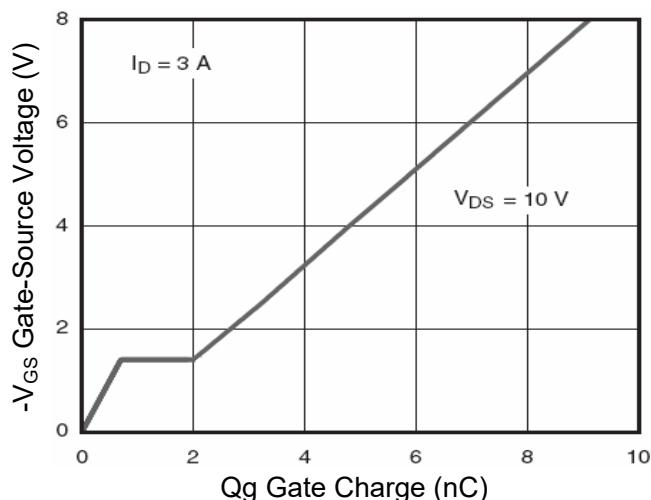
### Typical Characteristic Curves





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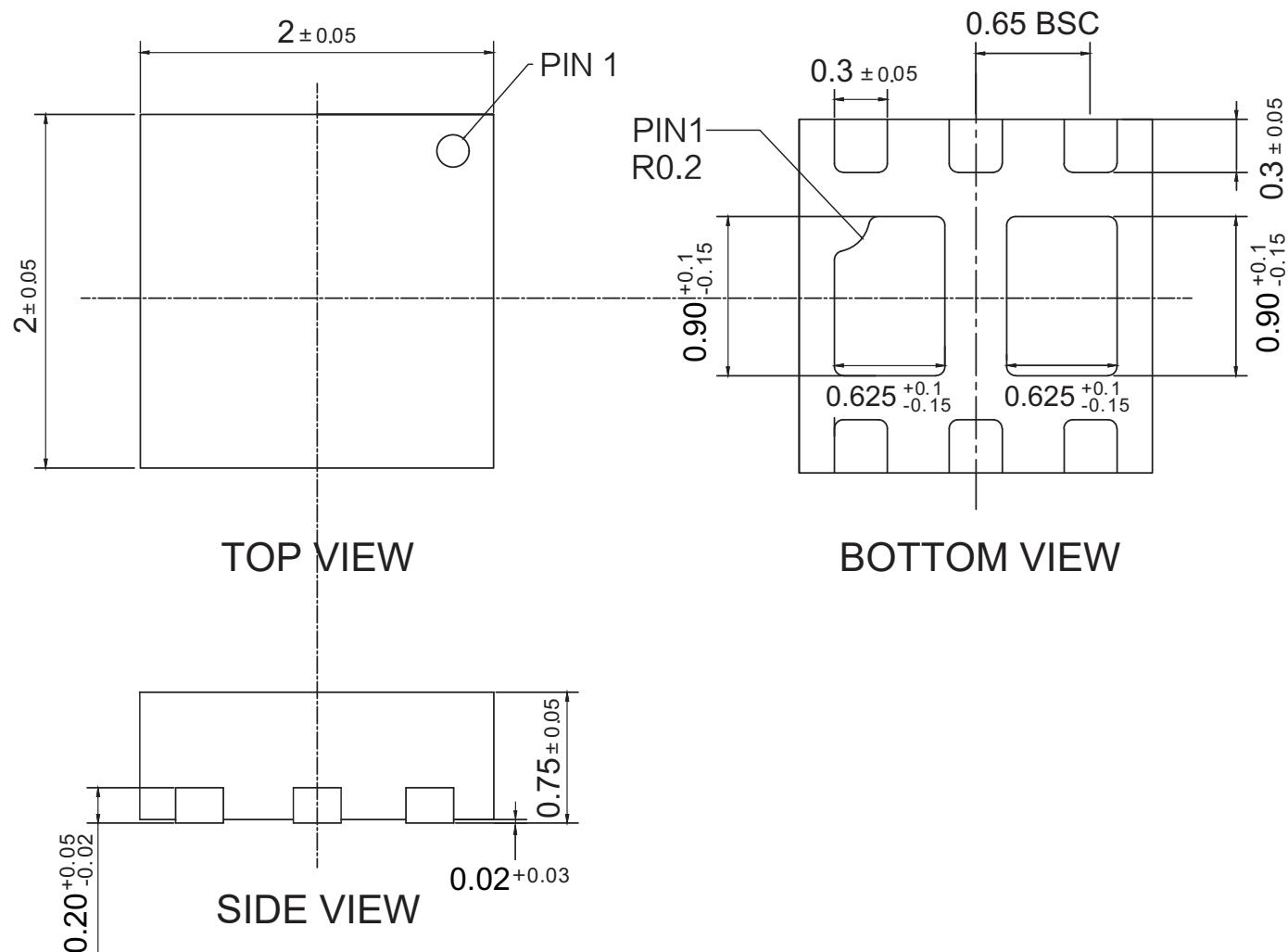




### Package Outline

DFN2x2-6L-0002

Dimensions in mm



### Ordering Information

Device	Package	Shipping
PJM03DP20DFA	DFN2x2A-6L	3,000PCS/Reel&7inches