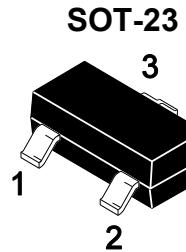


# MMBTRC101SS~MMBTRC106SS

## NPN Digital Transistor

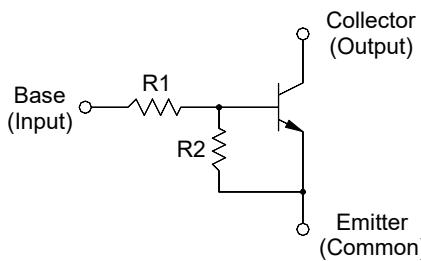
### Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process



### Equivalent Circuit

1.Base 2.Emitter 3.Collector



### Resistor Values/Marking Code

Type	R1 (KΩ)	R2 (KΩ)	Marking Code
MMBTRC101SS	4.7	4.7	1BR
MMBTRC102SS	10	10	2BR
MMBTRC103SS	22	22	3BR
MMBTRC104SS	47	47	4BR
MMBTRC105SS	2.2	47	5BR
MMBTRC106SS	4.7	47	6BR

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ )

Parameter	Symbol	Value	Unit
Output Voltage	$V_O$	50	V
Input Voltage	$V_I$	20,-10	V
		30,-10	
		40,-10	
		40,-10	
		12,-5	
		20,-5	
Output Current	$I_O$	100	mA
Maximum Power Dissipation	$P_D$	200	mW
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-55 to +150	°C



# MMBTRC101SS~MMBTRC106SS

## NPN Digital Transistor

### Electrical Characteristics ( $T_A=25^\circ\text{C}$ )

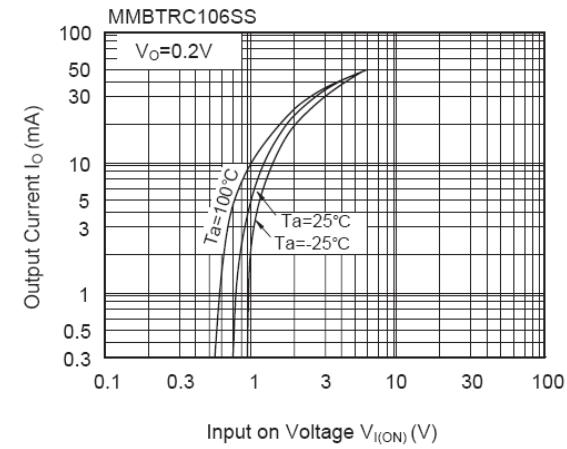
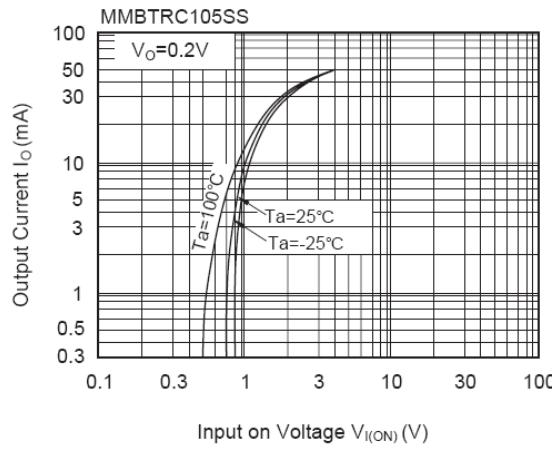
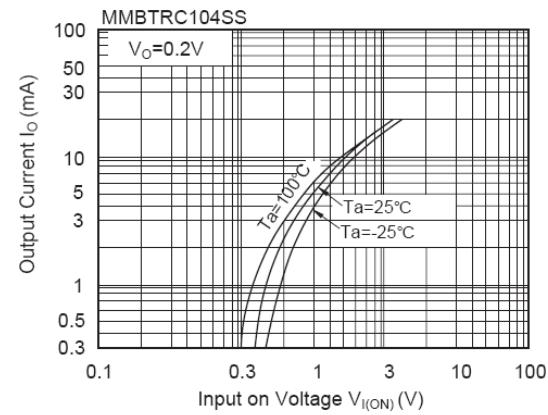
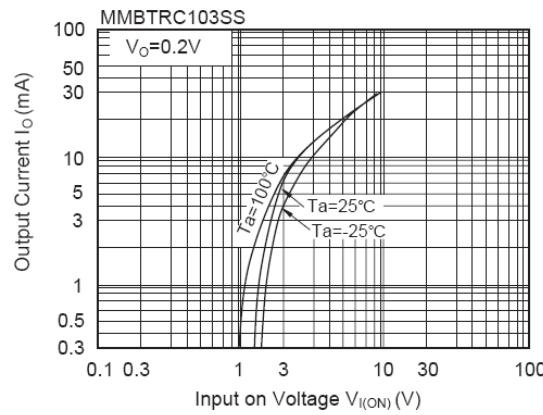
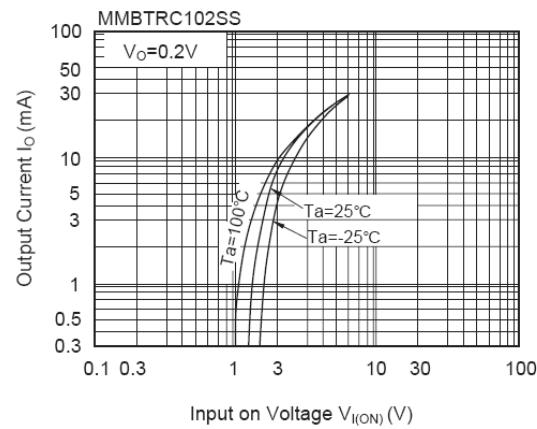
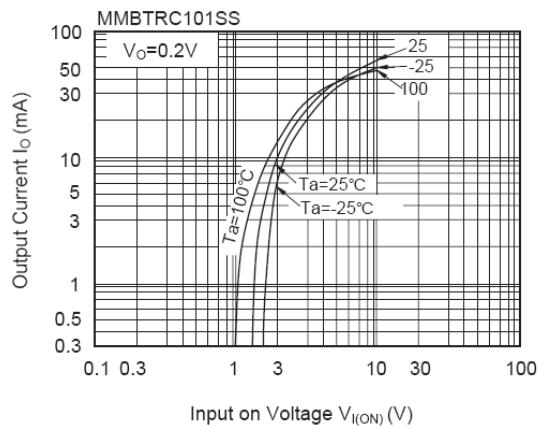
Parameter	Symbol	Min.	Typ.	Max.	Unit
DC Current Gain at $V_O = 5 \text{ V}$ , $I_O = 10 \text{ mA}$	$G_I$	30	--	--	
		50	--	--	
		70	--	--	
		80	--	--	
		80	--	--	
		80	--	--	
Output Cutoff Current at $V_O = 50 \text{ V}$	$I_{O(OFF)}$	--	--	500	nA
Input Current at $V_I = 5 \text{ V}$	$I_I$	--	--	1.8	
		--	--	0.88	
		--	--	0.36	mA
		--	--	0.18	
		--	--	3.6	
		--	--	1.8	
Output Voltage (ON) at $I_O = 10 \text{ mA}$ , $I_I = 0.5 \text{ mA}$	$V_{O(ON)}$	--	--	0.3	V
Input Voltage (ON) at $V_O = 0.2 \text{ V}$ , $I_O = 5 \text{ mA}$	$V_{I(ON)}$	--	--	2	
		--	--	2.4	
		--	--	3	
		--	--	5	
		--	--	1.1	
		--	--	1.3	
Input Voltage (OFF) at $V_O = 5 \text{ V}$ , $I_O = 0.1 \text{ mA}$	$V_{I(OFF)}$	1	--	--	
		0.5	--	--	
Transition Frequency at $V_O = 10 \text{ V}$ , $I_O = 5 \text{ mA}$	$f_T$	--	200	--	MHz

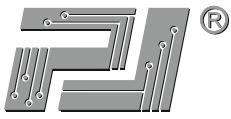


# MMBTRC101SS~MMBTRC106SS

## NPN Digital Transistor

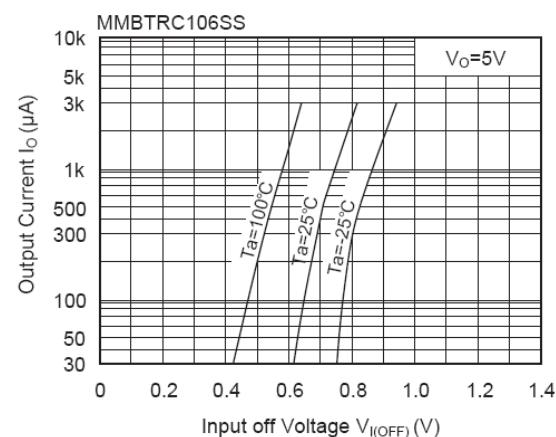
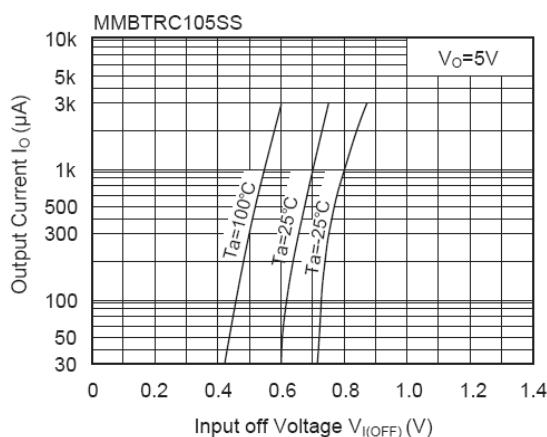
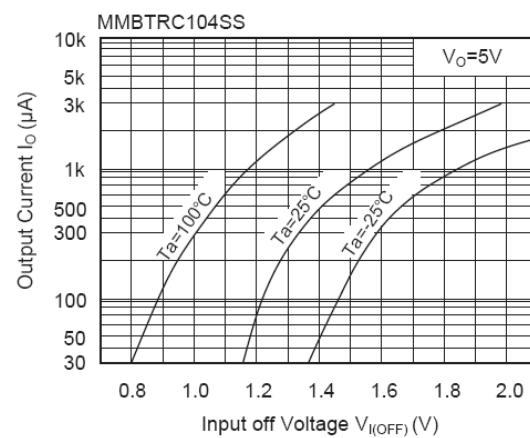
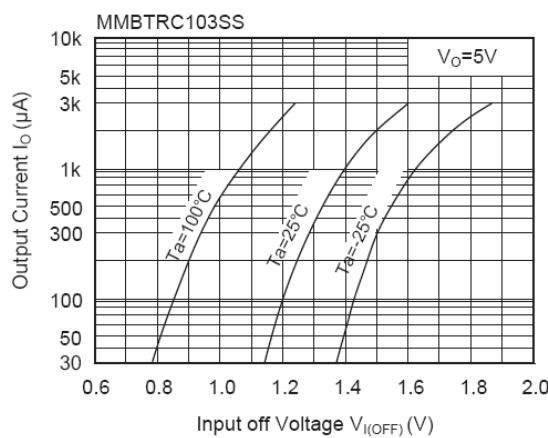
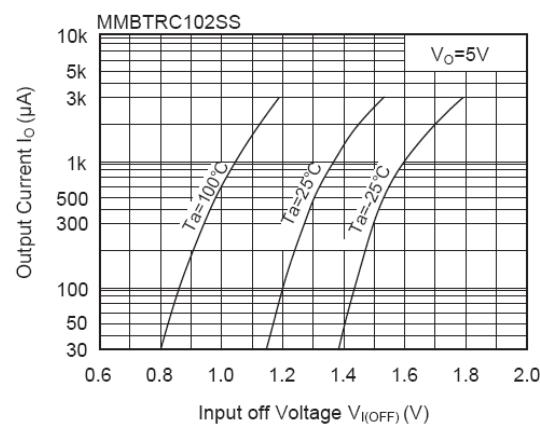
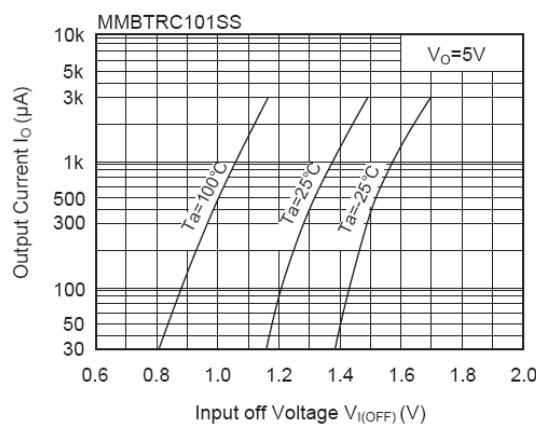
### Typical Characteristic Curves

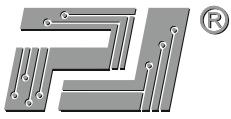




# MMBTRC101SS~MMBTRC106SS

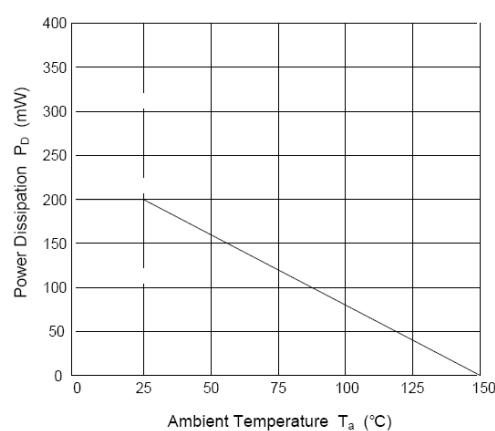
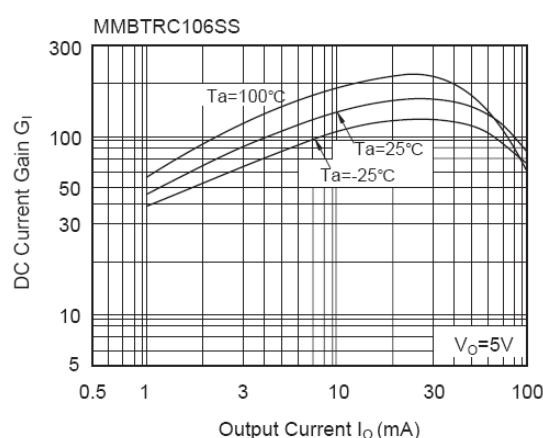
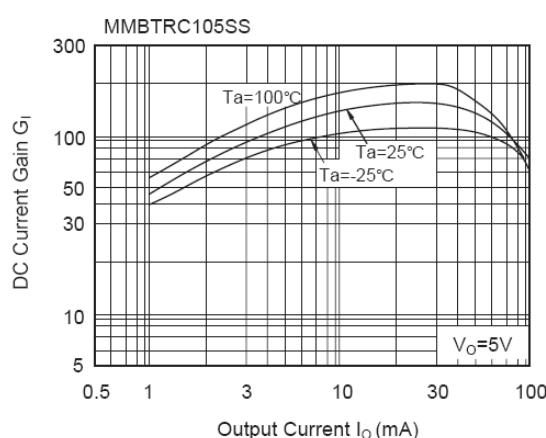
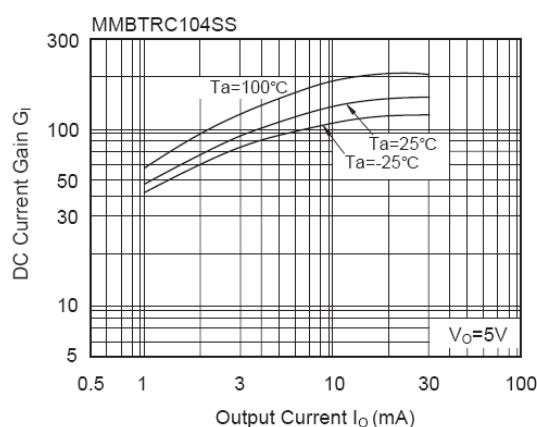
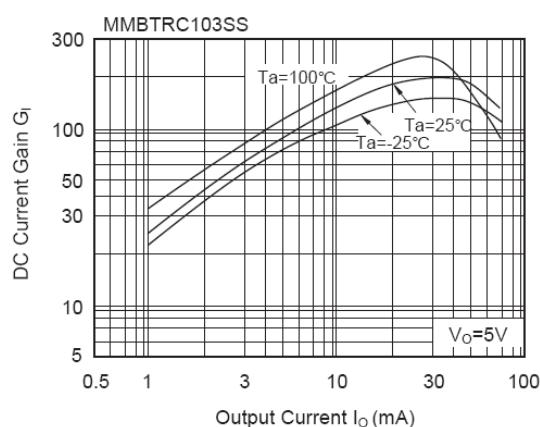
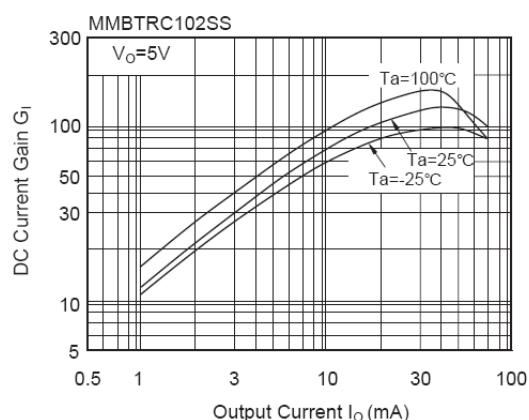
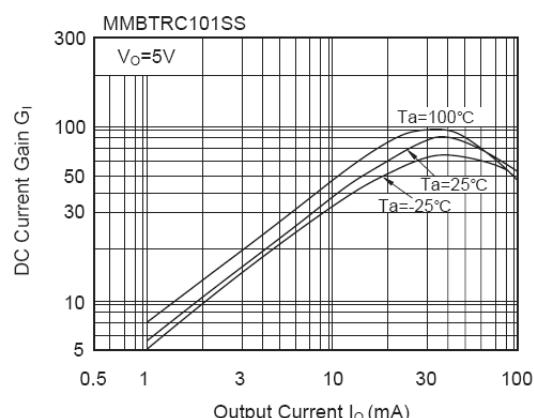
## NPN Digital Transistor





# MMBTRC101SS~MMBTRC106SS

## NPN Digital Transistor





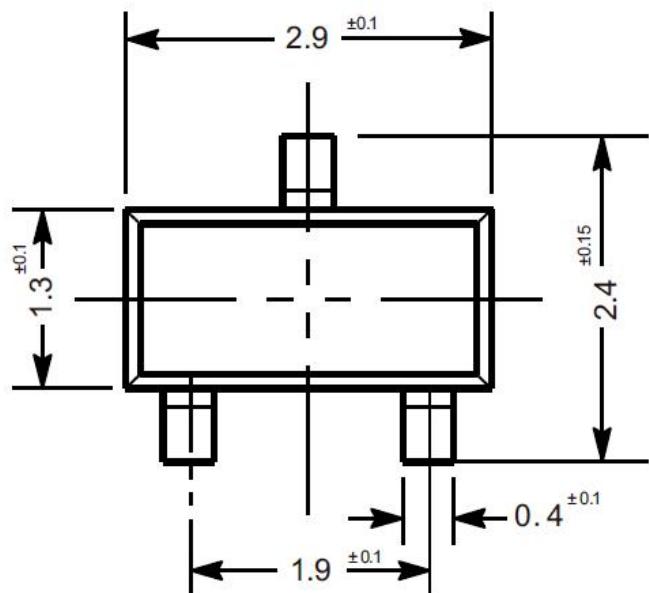
# MMBTRC101SS~MMBTRC106SS

## NPN Digital Transistor

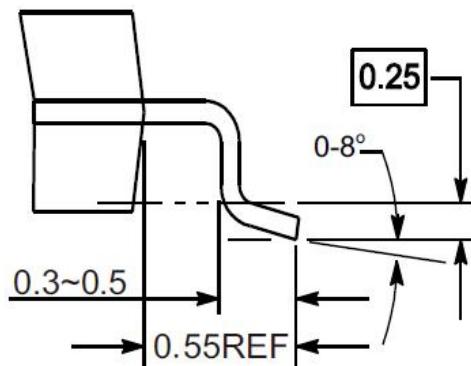
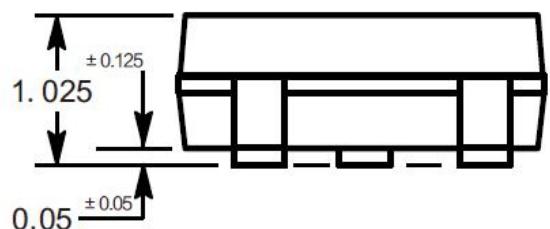
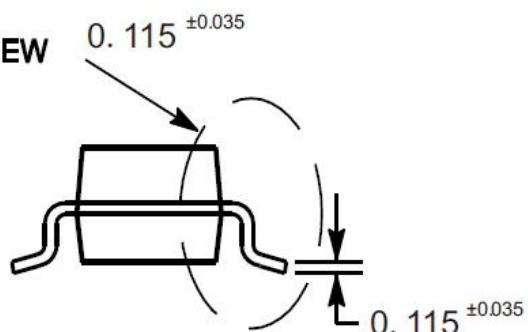
### Package Outline

SOT-23

Dimensions in mm



SEE VIEW



VIEW C

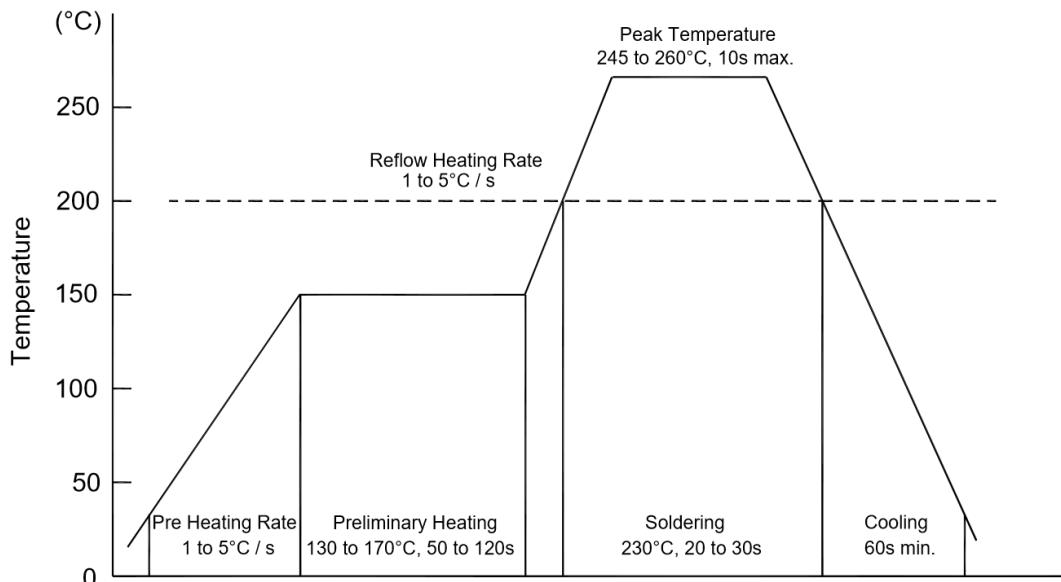
### Ordering Information

Device	Package	Shipping
MMBTRC101SS~MMBTRC106SS	SOT-23	3,000PCS/Reel&7inches



## Conditions of Soldering and Storage

### ◆ Recommended condition of reflow soldering



Recommended peak temperature is over 245 °C. If peak temperature is below 245 °C, you may adjust the following parameters:

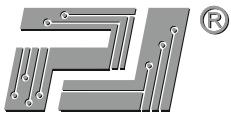
- Time length of peak temperature (longer)
- Time length of soldering (longer)
- Thickness of solder paste (thicker)

### ◆ Conditions of hand soldering

- Temperature: 370 °C
- Time: 3s max.
- Times: one time

### ◆ Storage conditions

- **Temperature**  
5 to 40 °C
- **Humidity**  
30 to 80% RH
- **Recommended period**  
One year after manufacturing

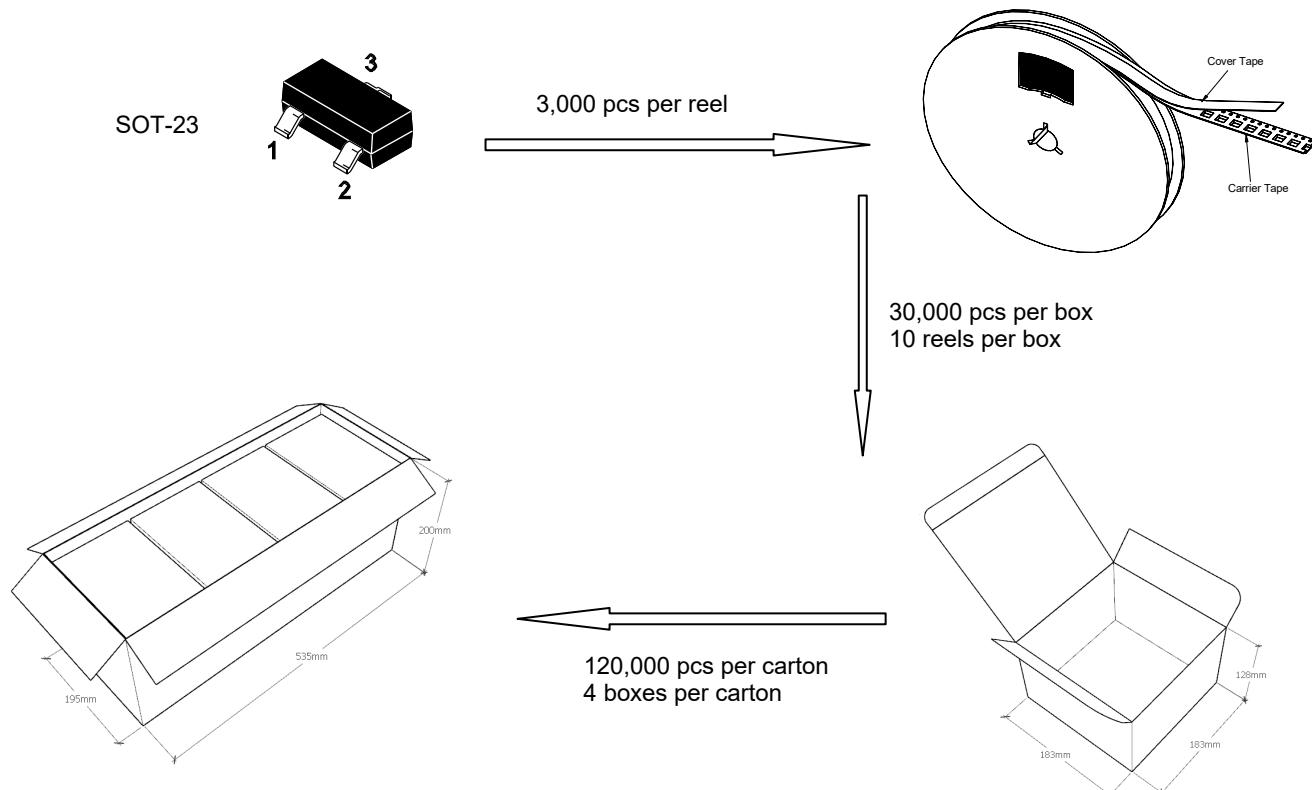


# MMBTRC101SS~MMBTRC106SS

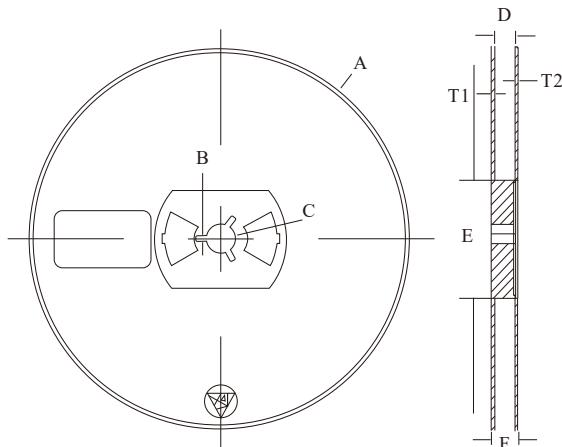
## NPN Digital Transistor

### Package Specifications

- The method of packaging



### ◆ Embossed tape and reel data



Symbol	Value (unit: mm)
A	$\varnothing 177.8 \pm 1$
B	$2.7 \pm 0.2$
C	$\varnothing 13.5 \pm 0.2$
E	$\varnothing 54.5 \pm 0.2$
F	$12.3 \pm 0.3$
D	$9.6 +2/-0.3$
T1	$1.0 \pm 0.2$
T2	$1.2 \pm 0.2$

Reel (7")

